

digital

DMC11

Engineering Drawings (ANNOTATED)

Digital Equipment Corporation

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ELECTRICAL						MECHANICAL							
CUSTOMER PRINT SET		NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO./FILE DATE	CUSTOMER PRINT SET		NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO./FILE DATE
1	D-UA-DMC11-0-0			3	INTERPROCESSOR LINK		1	D-UA-DMC11-0-0			3	INTERPROCESSOR LINK	
	A-PL-DMC11-0-7			1	DMC11 SHIPPING LIST								
	B-TC-DMC11-0-8			1	FIELD MAINTENANCE PRINT SET								
	A-SP-DMC11-0-5			30	TEST AND ACCEPTANCE SPECIFICATION								
2	B-DD-M8200-0			1	INTERPROCESSOR LINK UP		5	C-UA-BC08S-0-0			1	CABLE, BC08S	
3	B-DD-M8201-0			1	IPL SLU LOW		6	D-UA-BC05C-0-0			1	CABLE, BC05C	
4	B-DD-M8202-0			1	IPL SLU HI		7	C-UA-BC05Z-0-0			1	CABLE, BC05Z	
8	D-CS-H325-0-0			1	H325 TEST CONNECTOR		9	C-UA-BC03N-0-0			1	CABLE, BC03N	
							10	A-SP-3700283-0-0				PACKAGING INSTRUCTIONS	
								A-PS-9906040-0-0				FULL TELESCOPE CARTON ASSY.	
								A-PS-9905816 0-0				BOX, FOLDING, REVERSE TUCK	
								A-PS-9905809-0-0				FULL TELESCOPE CARTON ASSY.	
								A-PS-9905729-0-0				TAPE, CARTON SEALING	
							11	D-UA-H3250			1	H3250 TEST CONNECTOR	

CUSTOMER PRINT SET CODES

X = PRINT OF DOCUMENT INCLUDED IN PRINT SET
C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT
S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED

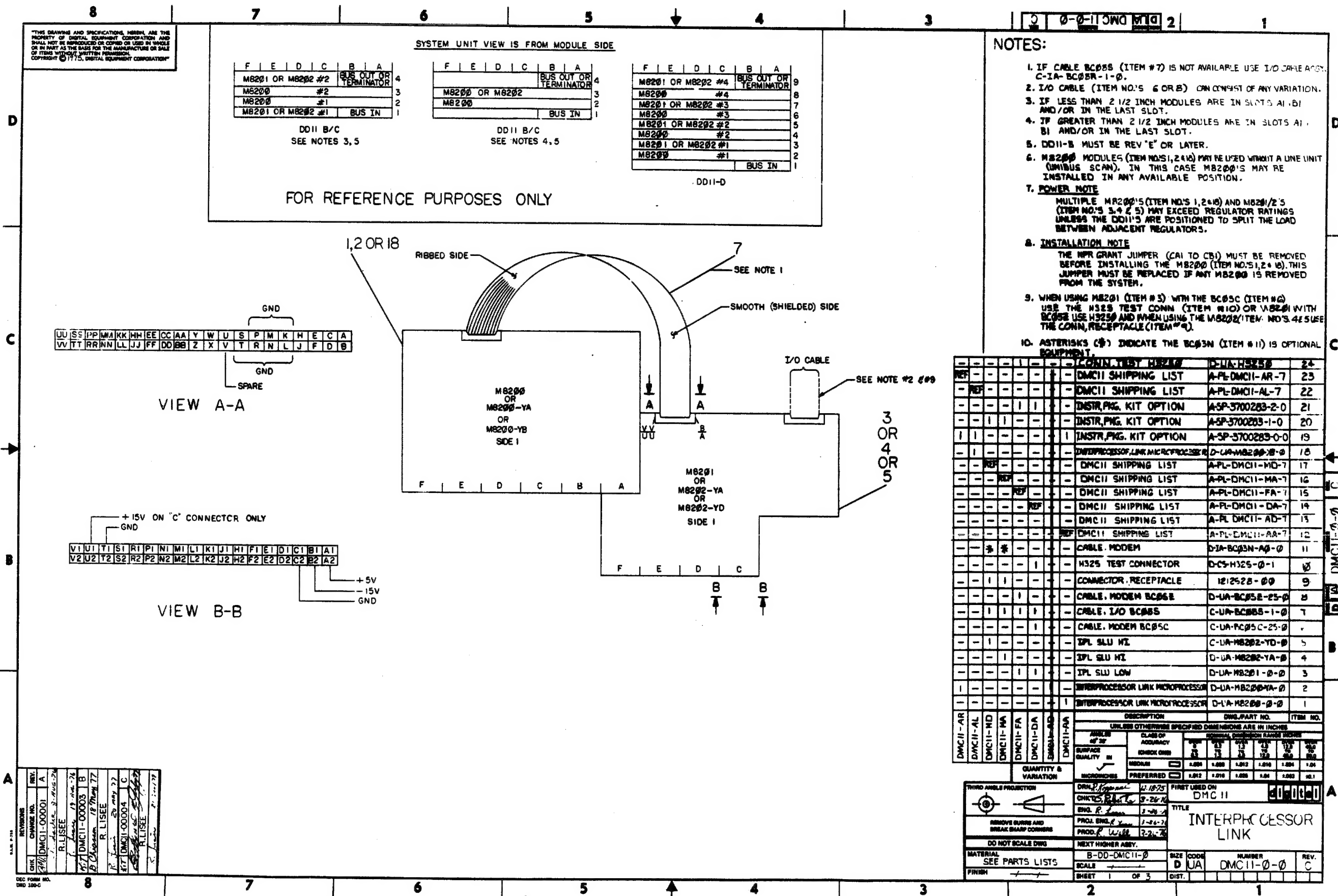
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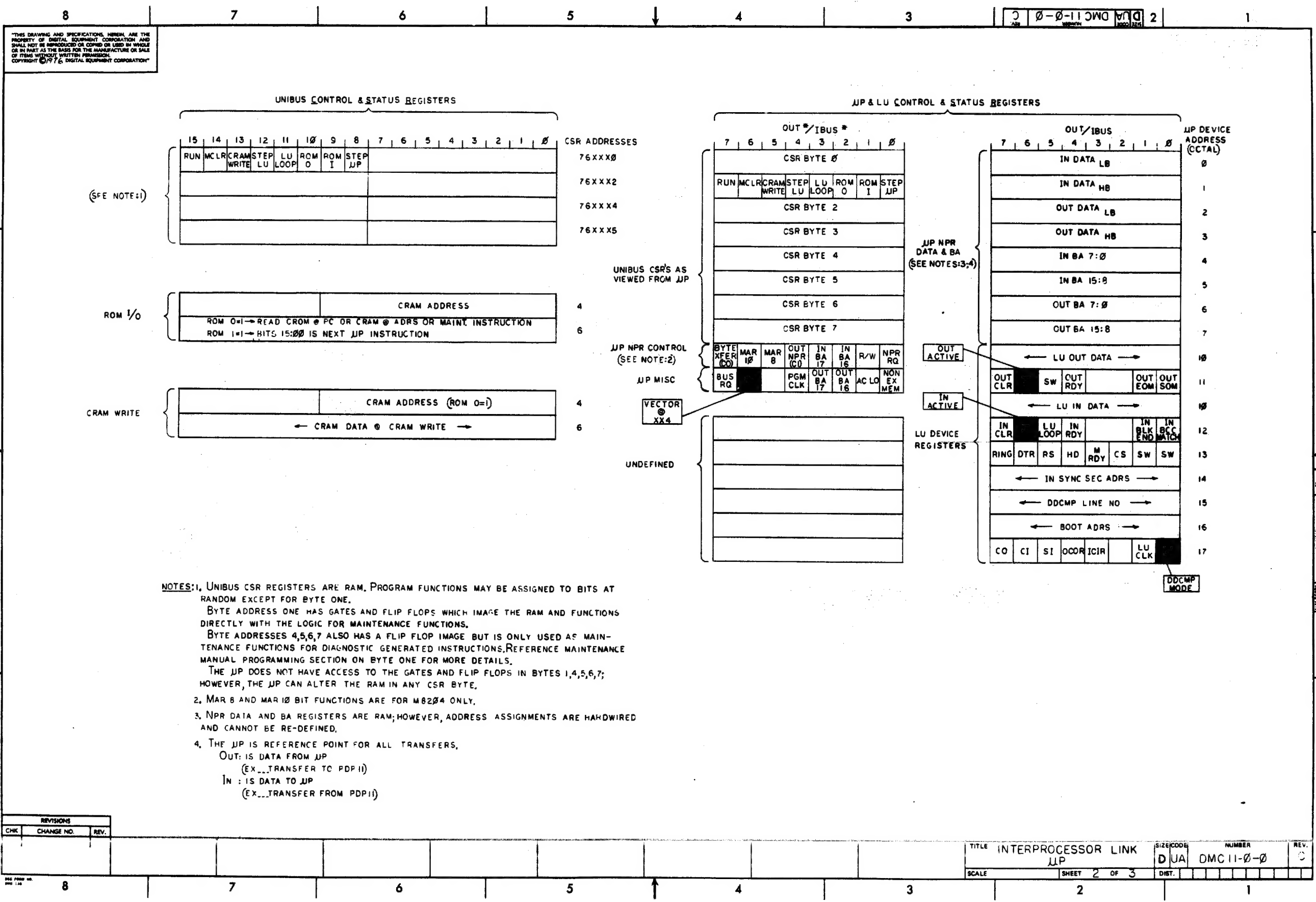
SHEET 3 OF 3

SIZE CODE
B DD

NUMBER
DMC11-0

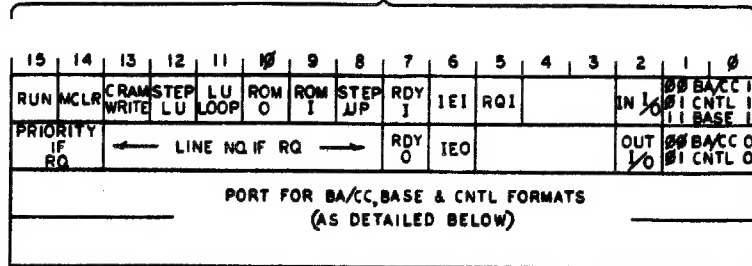
REV
C



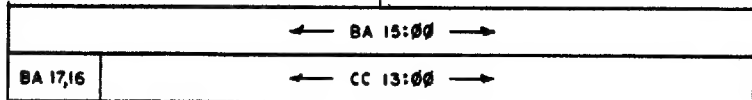


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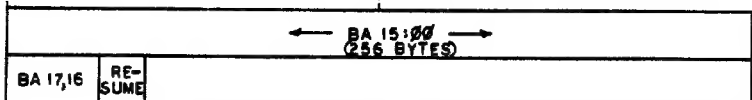
UNIBUS CONTROL & STATUS REGISTERS



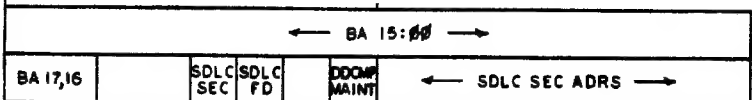
BA/CC



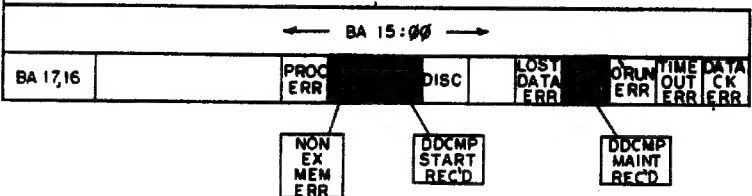
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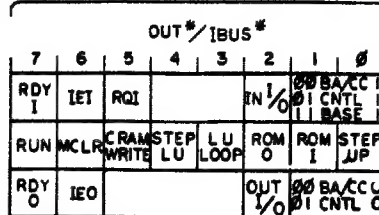
CNTL 1



CNTL 0

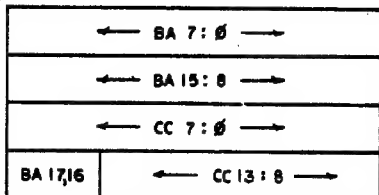


JUP & LU CONTROL & STATUS REGISTERS

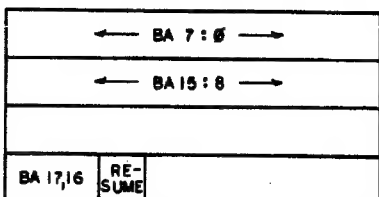


VECTOR
XX4

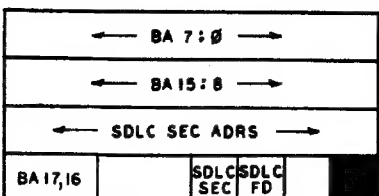
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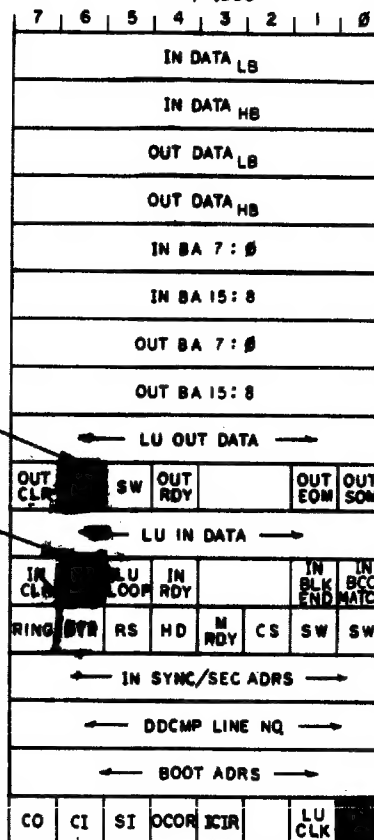
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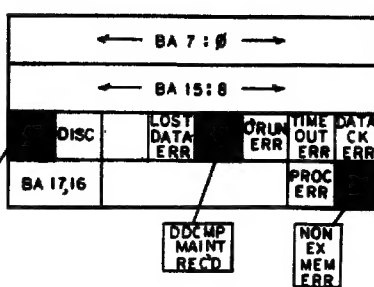
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OUT*/IBUS



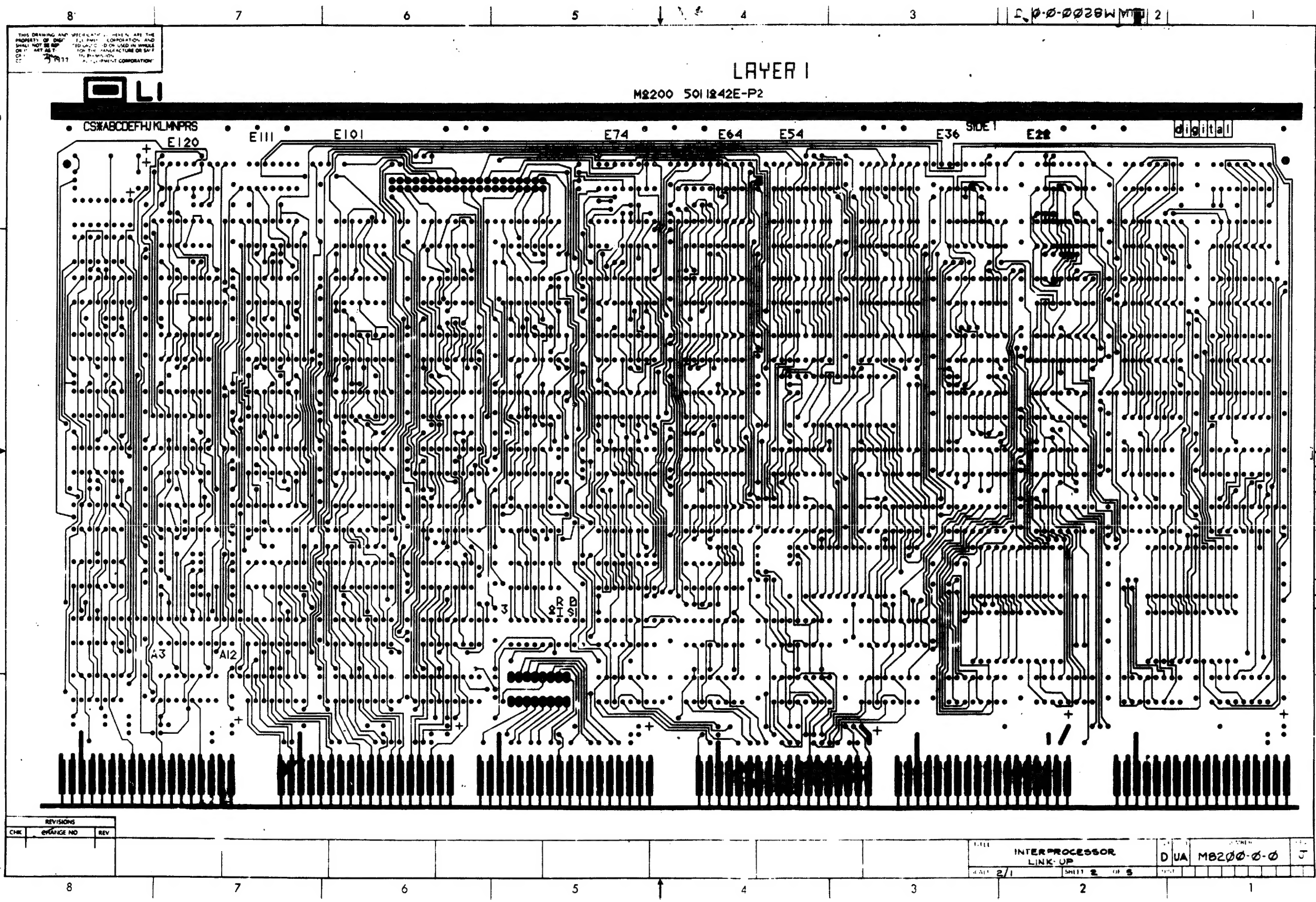
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REVISIONS		
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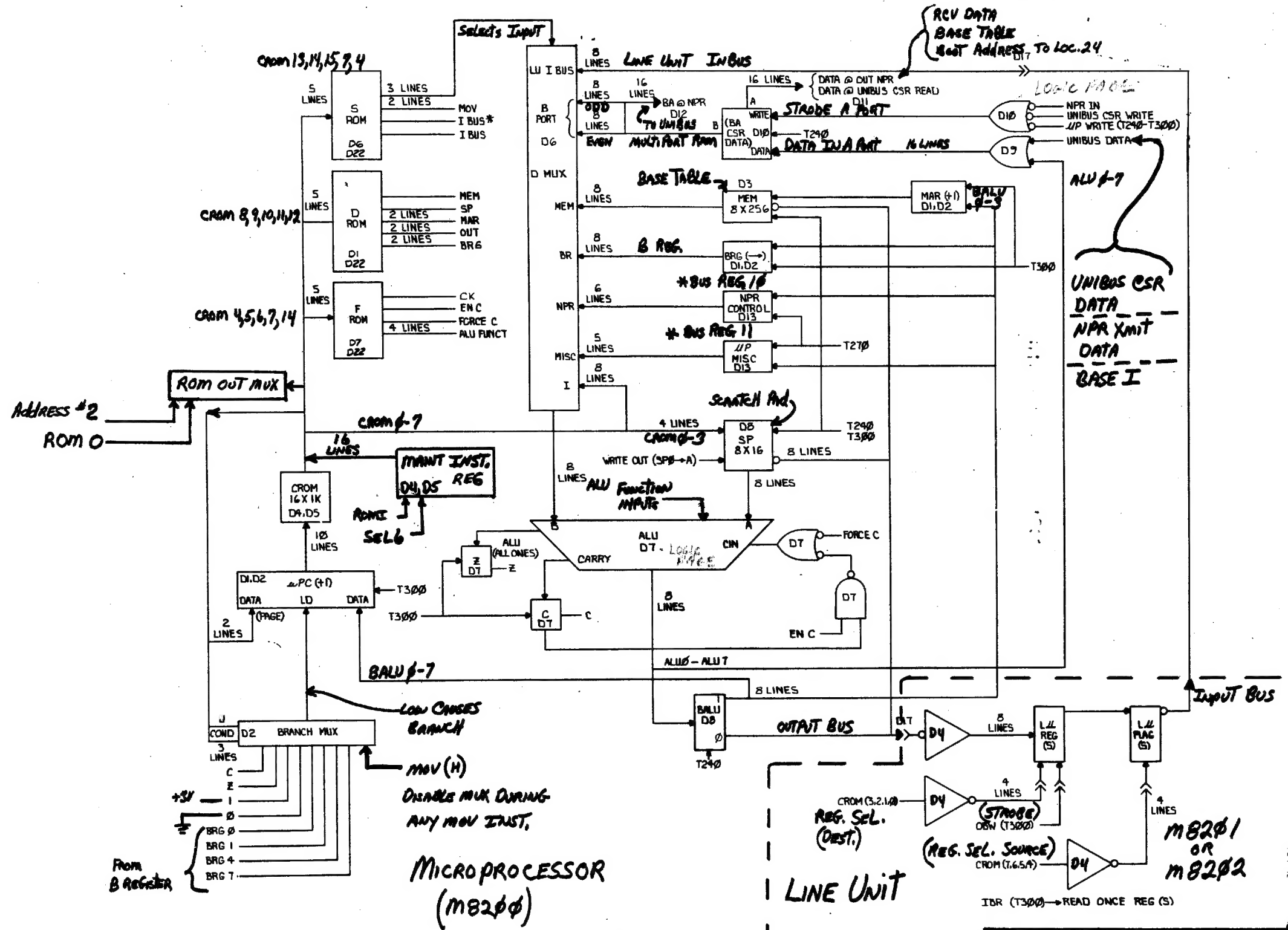
(BIT MAP FOR DDCMP PROTOCOL)

TITLE INTERPROCESSOR LINK JUP										SIZE CODE	NUMBER	REV.
SCALE										DUA	DMC 11-0-0	C
SHEET 3 OF 3										DWT.		



DMC-f

BLOCK DIAGRAM

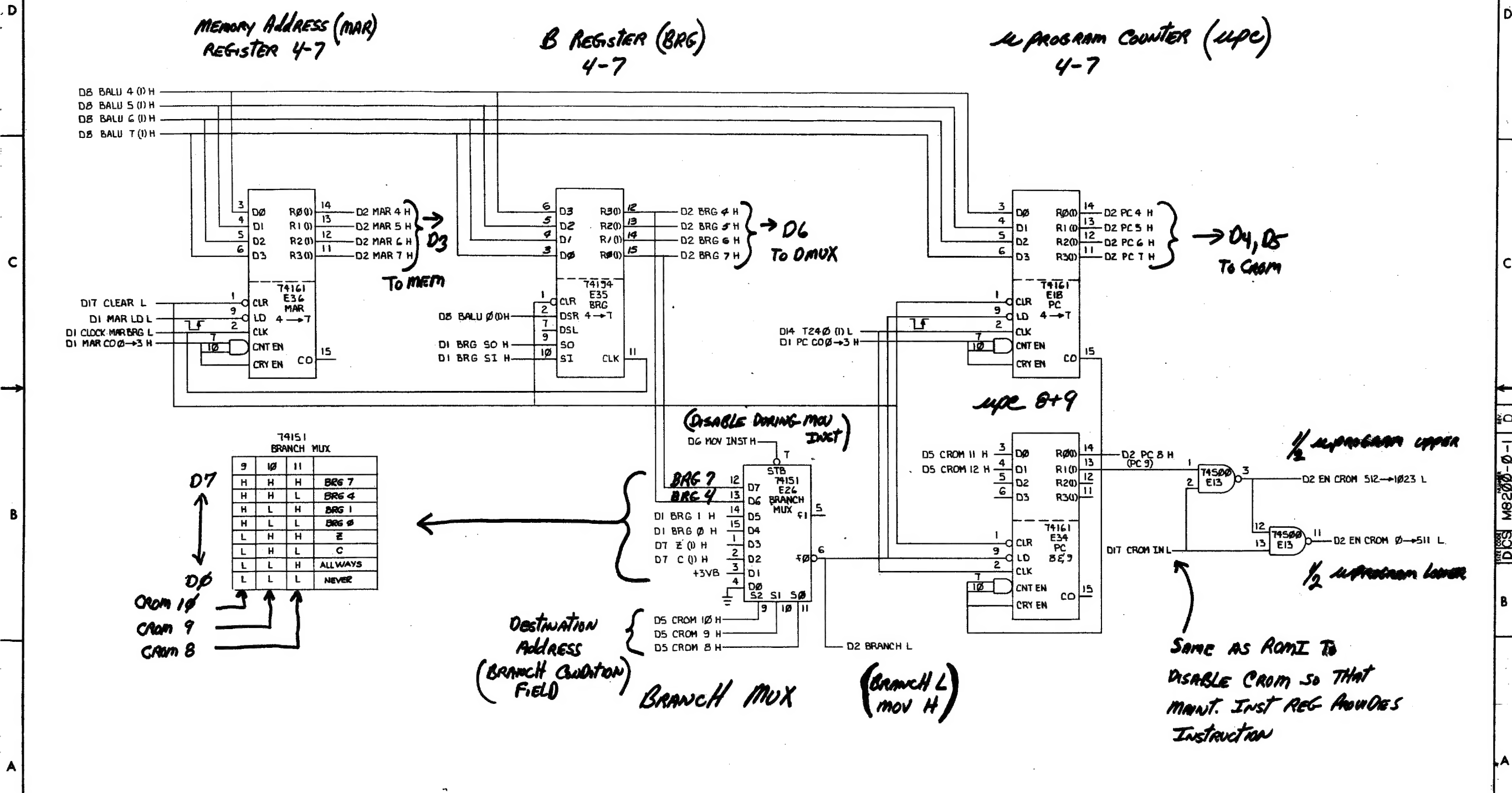


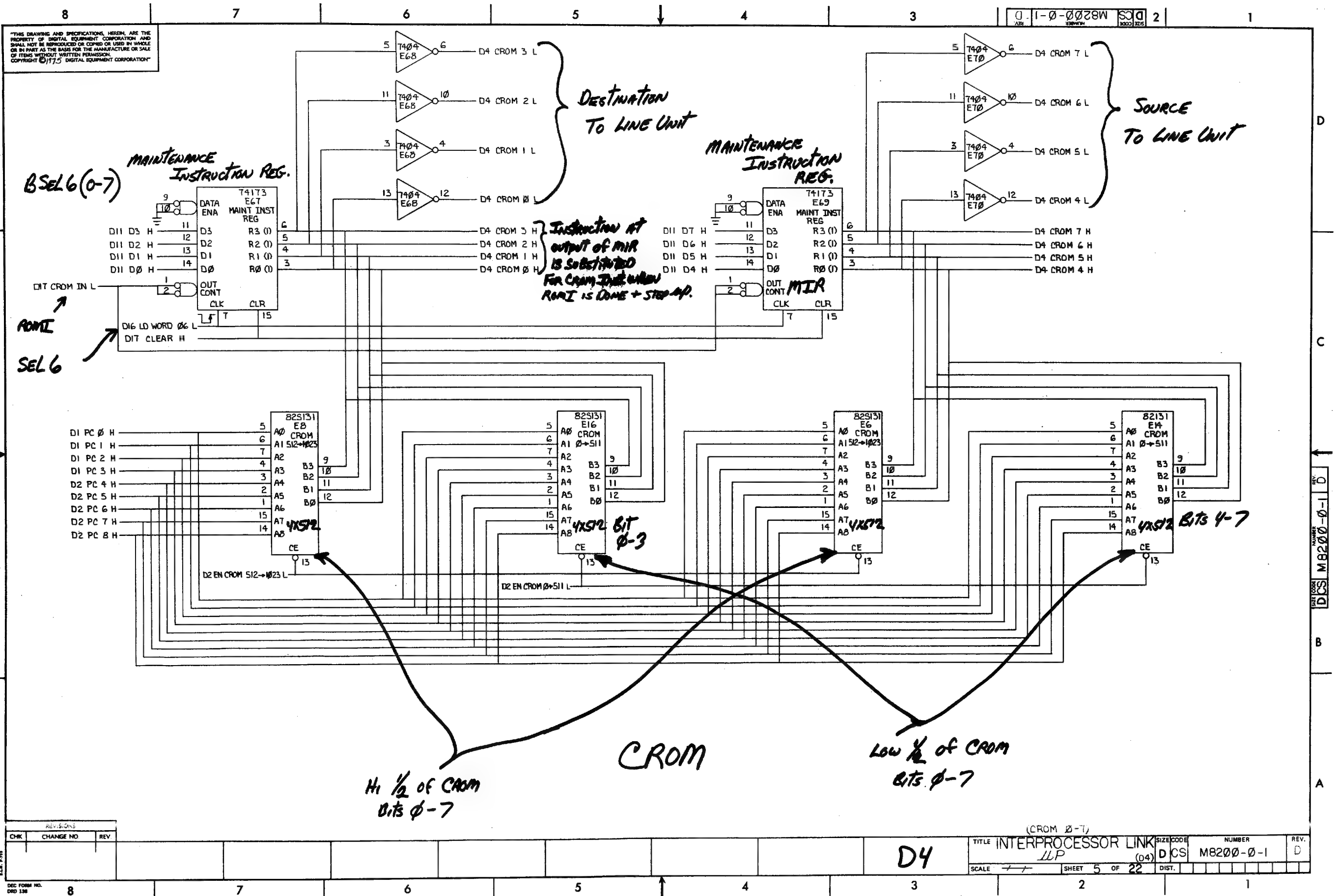
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7	7	7	7
8	8	8	8
9	9	9	9
10	10	10	10
11	11	11	11
12	12	12	12
13	13	13	13
14	14	14	14
15	15	15	15
16	16	16	16
17	17	17	17
18	18	18	18
19	19	19	19
20	20	20	20

REV. 11	7-16-75	FIRST USED ON	DMC 11	01000-01
TITLE	INTERPROCESSOR LINK			
NUMBER	(DO)			
SIZE	D	CS	M8200-0-1	REV. 11
DIST.	22			

Dmc-1

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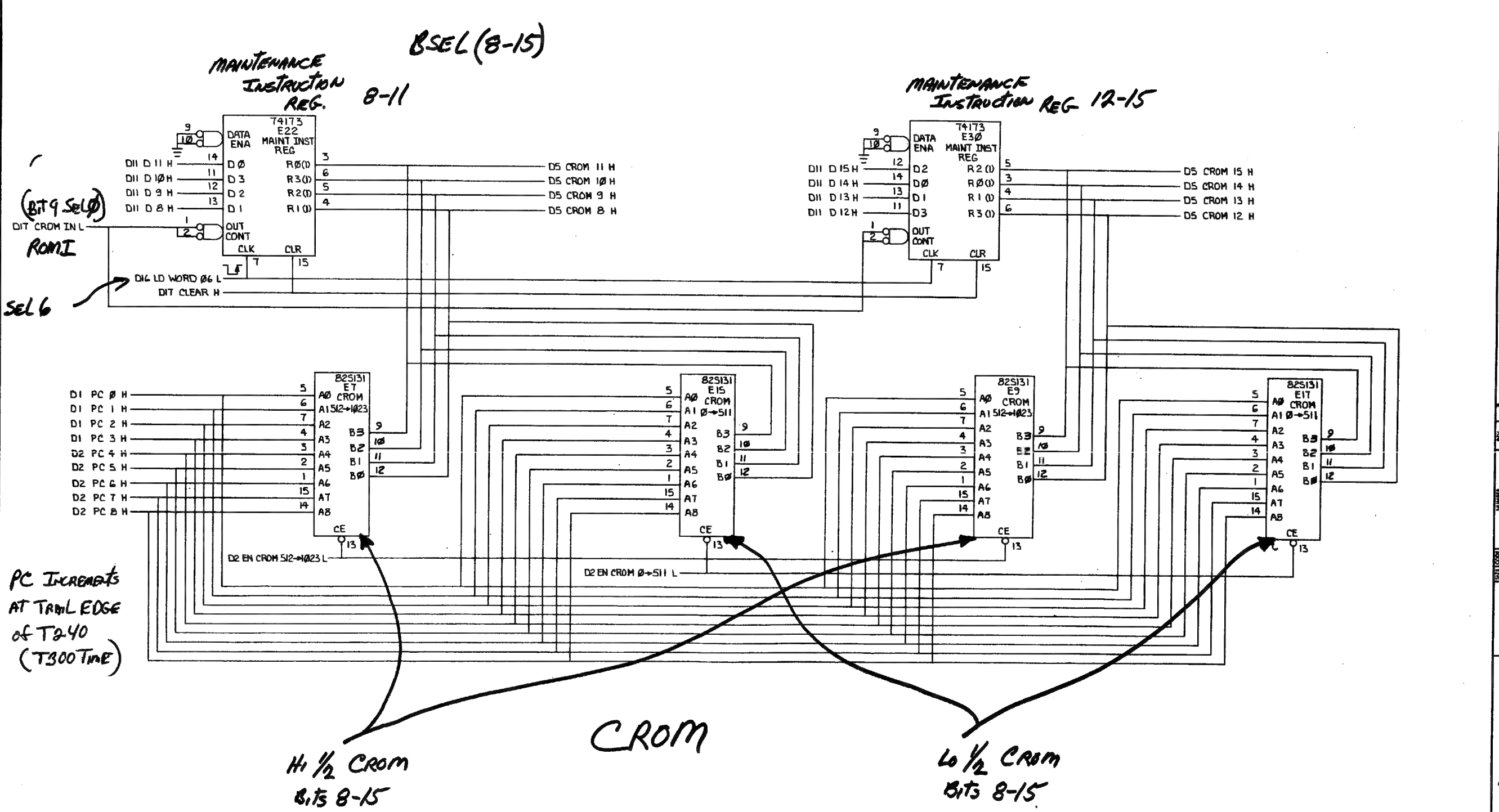


DMC-4

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DCS M8200-0-1

2



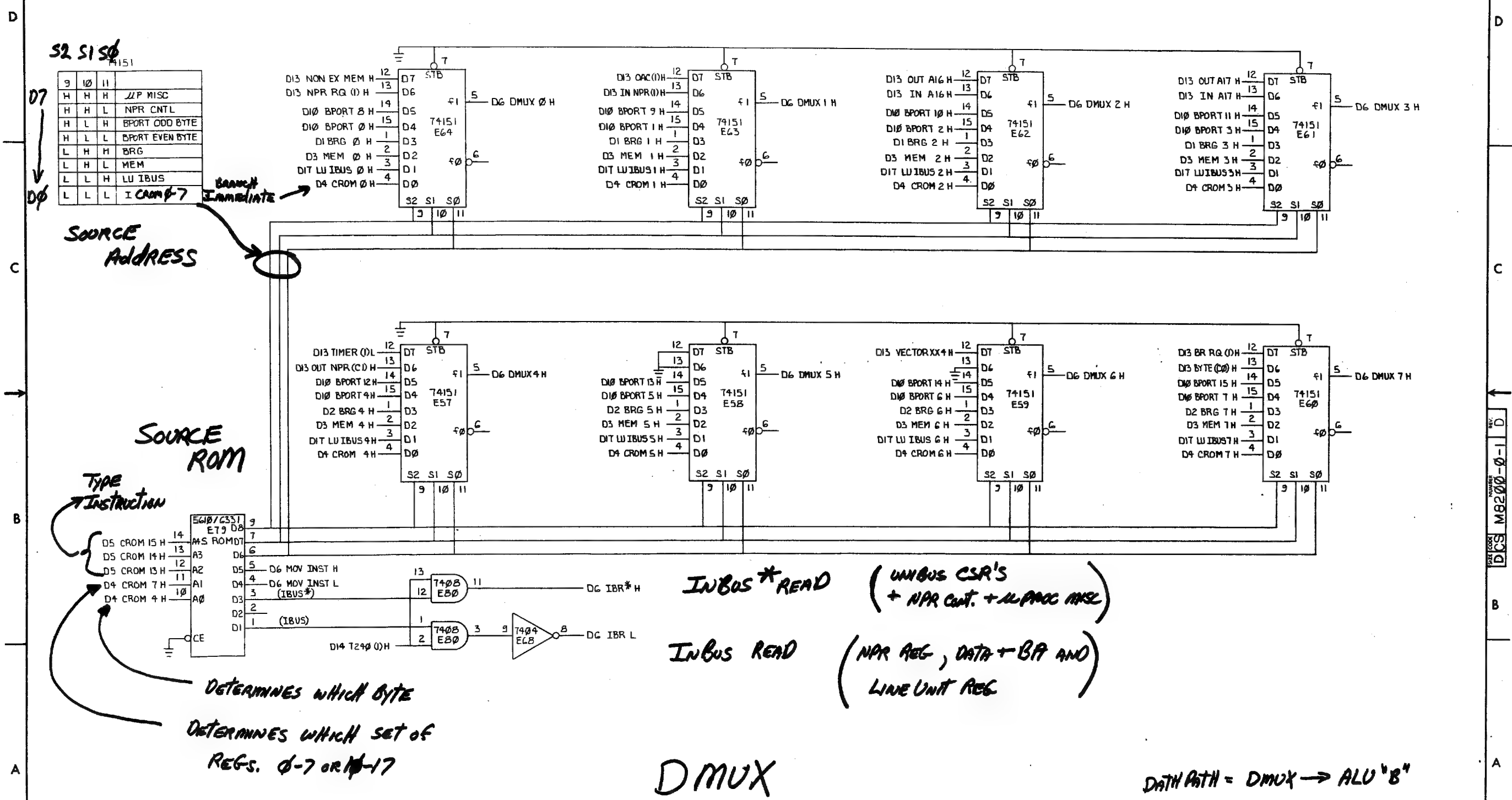
REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO. 000 136

TITLE		INTERPROCESSOR LINK	SIZE CODE	NUMBER	REV.
D5		LLP	(05)	DCS M8200-0-1	D
SCALE		++	SHEET	6	OF 22
DIST.					

DMC-5

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REVISIONS		
CHK	CHANGE NO.	REV.

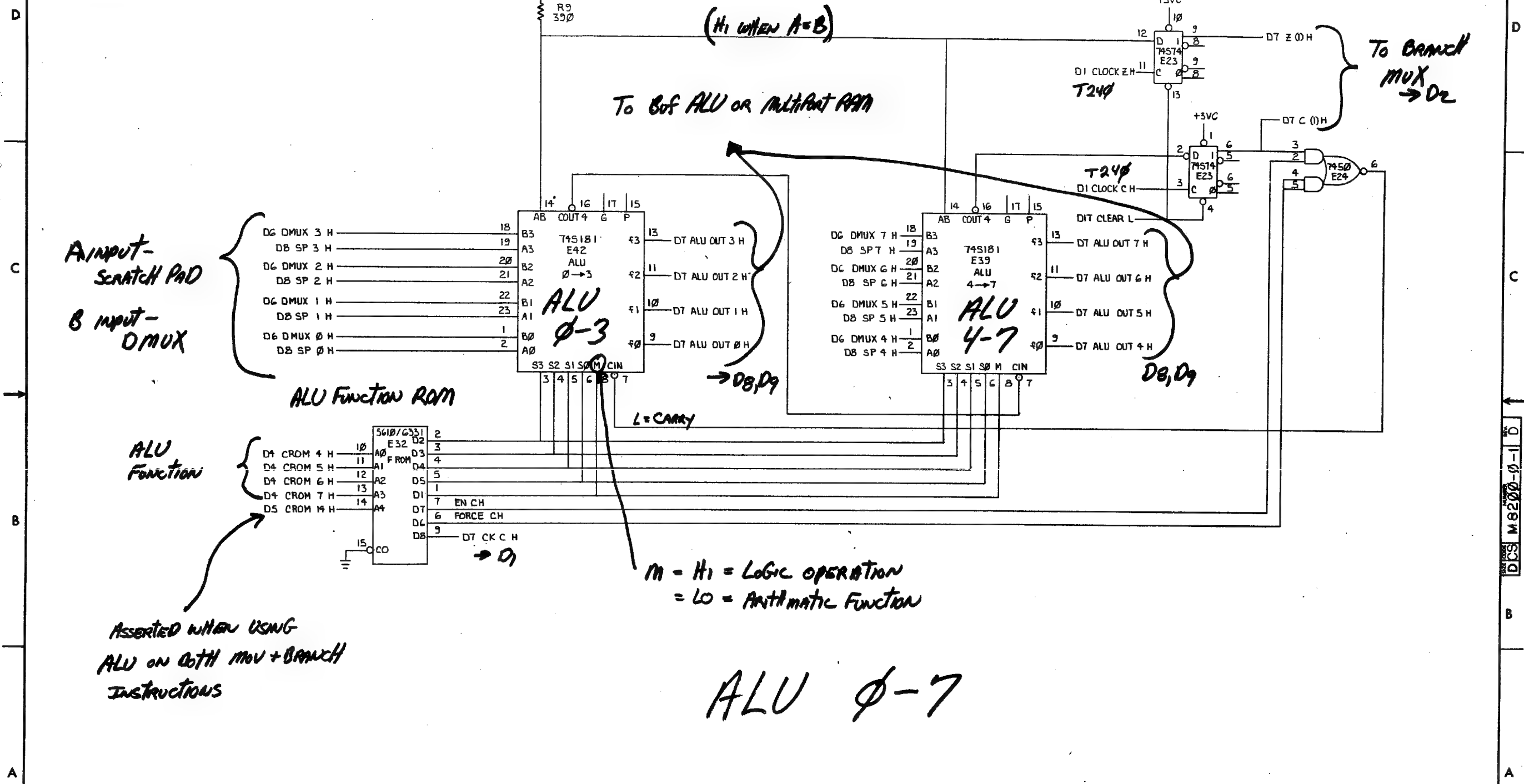
DEC FORM NO. 130

TITLE		SIZE CODE	NUMBER	REV.
INTERPROCESSOR LINK		(D6) DCS	M8200-0-1	0
SCALE		SHEET	OF	DIST.
++		7	22	

DMC-6

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1-0-0028W DCS 2

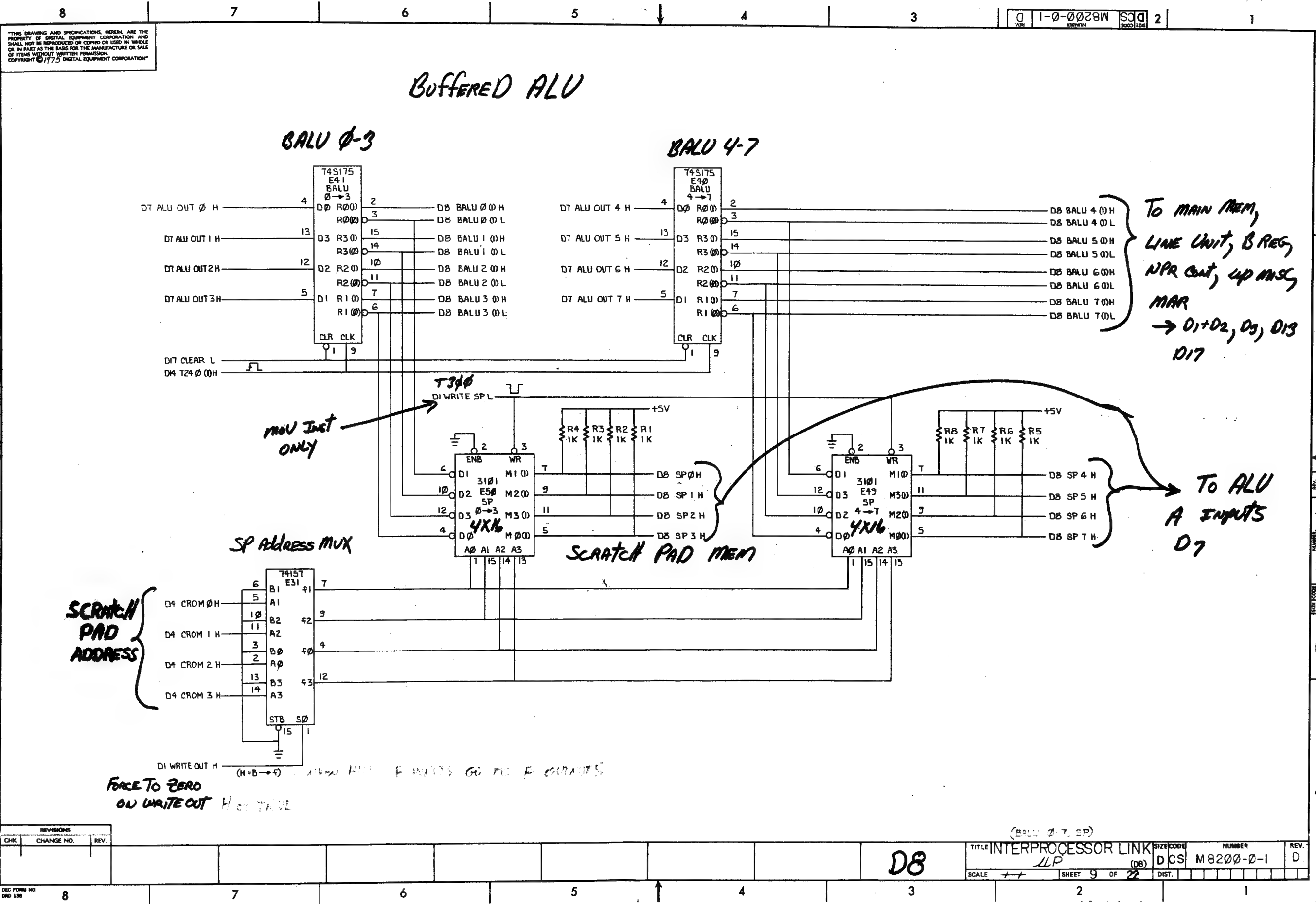


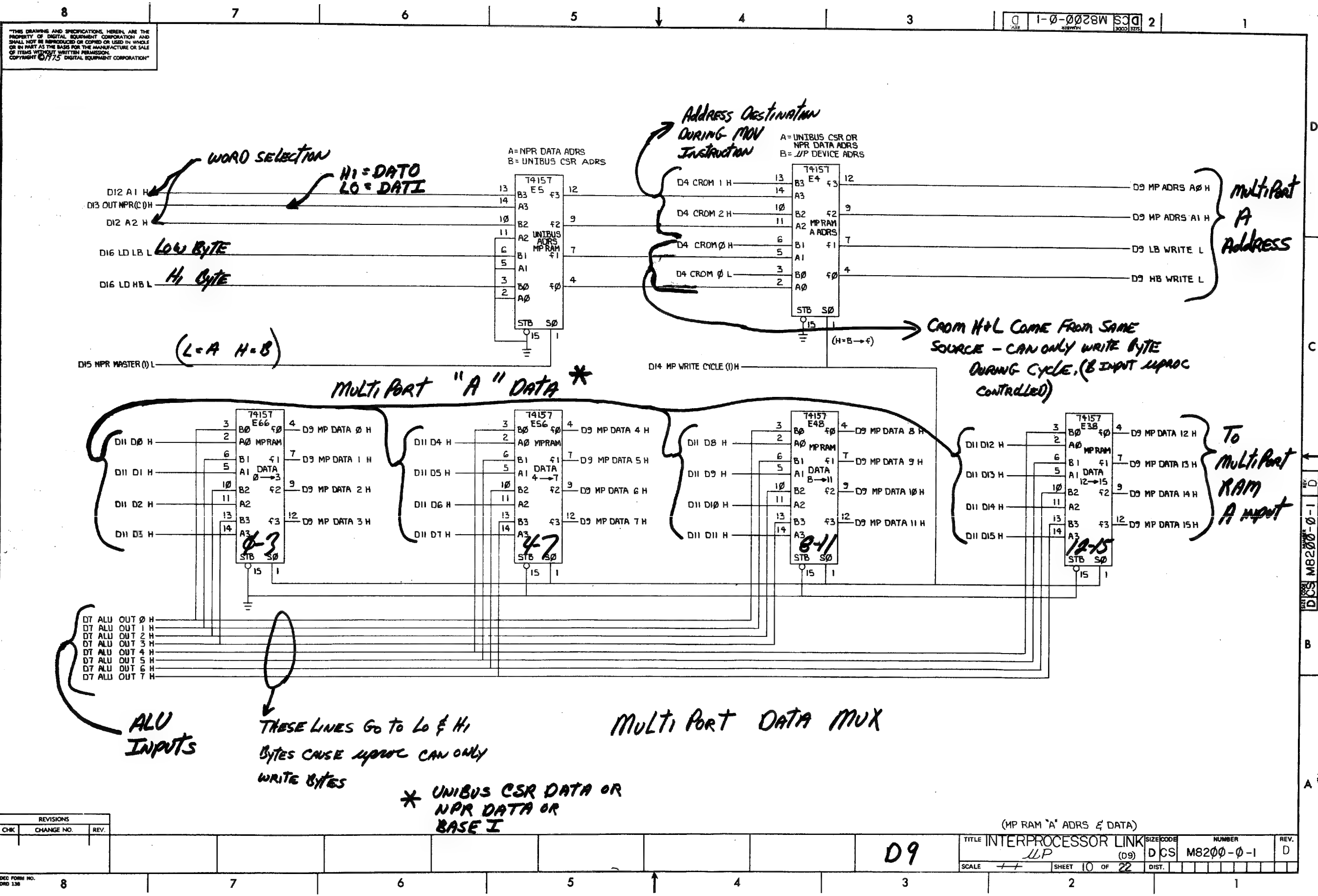
ALU 0-7

REVISIONS		
CHK	CHANGE NO.	REV.

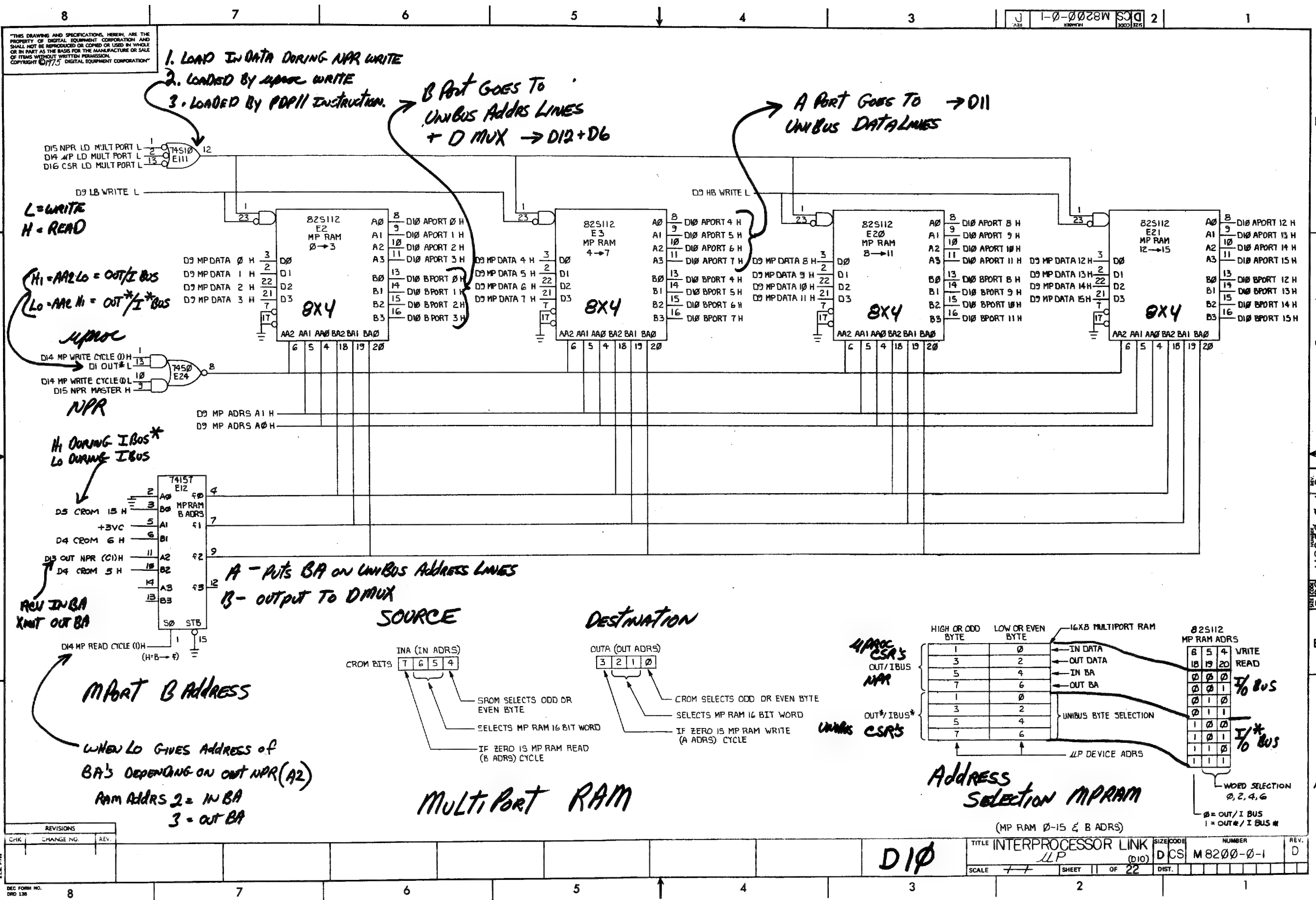
(ALU 0-7, C, Z, FROM)		TITLE	INTERPROCESSOR LINK	SIZE CODE	NUMBER	REV.
D7		LLP	(07)	DCS	M8200-0-1	D
SCALE	++	SHEET	8	OF	22	DIST.

DMC-7





DMC-9



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A = MultiPort
RAM DATA

B = CROM
Instruction

A₂ IS SEL 4 OR SEL 6

CROM OUT IS →
(MAINT. SEL 0 BIT 10)

(PORT DATA OR UP/NAR OUT)

MAINT MUX FOR CROM Inst.

UNIBUS
TRANSFERS

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE INTERPROCESSOR LINK		SIZE CODE DCS	NUMBER M8200-0-1	REV. D
SCALE	SHEET 12 OF 22	DIST.		

DMC-11



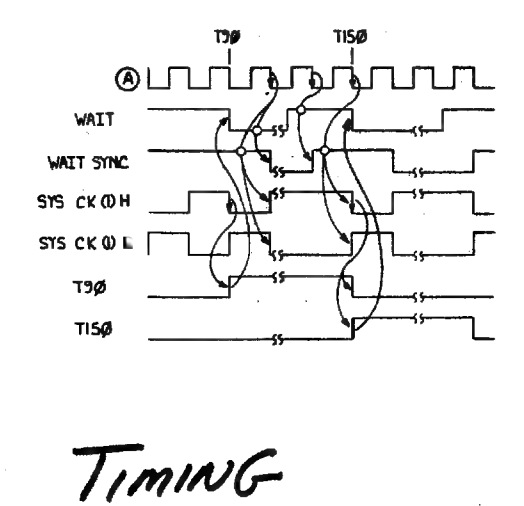
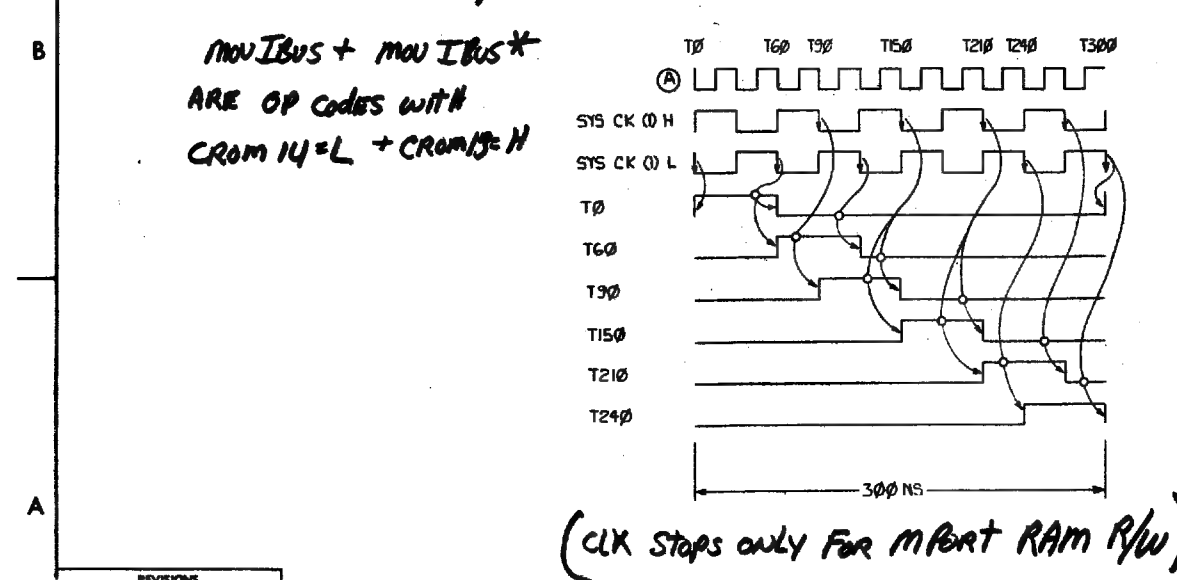
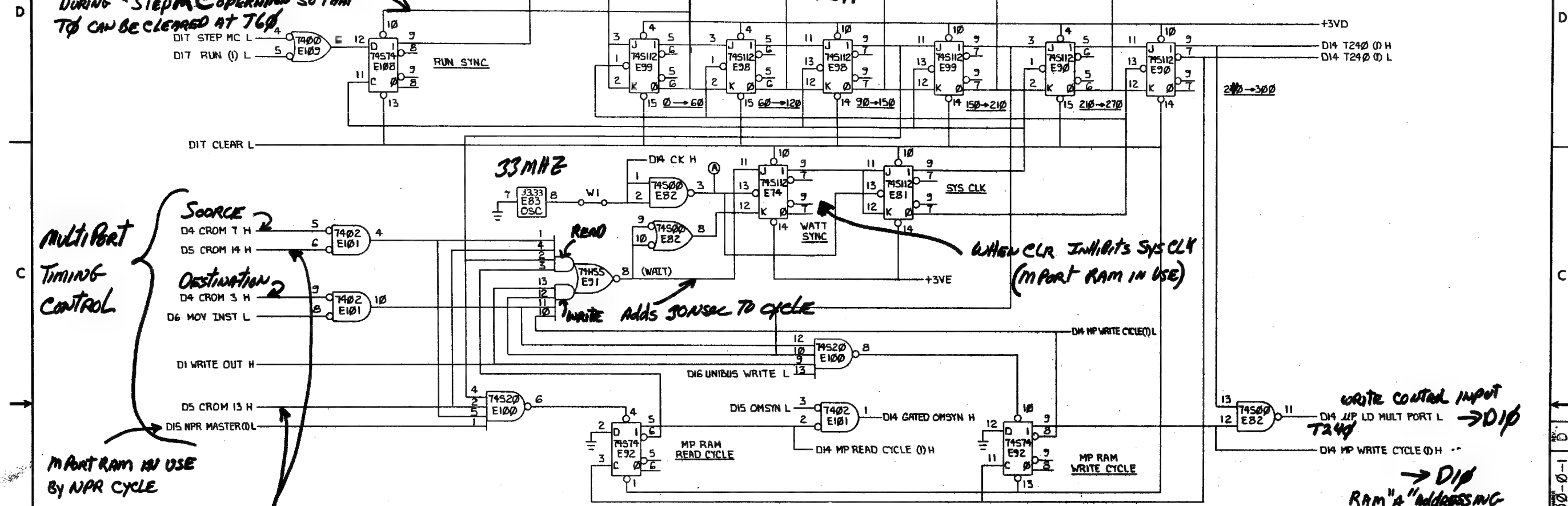
CHK	CHANGE NO.	REV.

DEC FORM NO. 8
DOD 158

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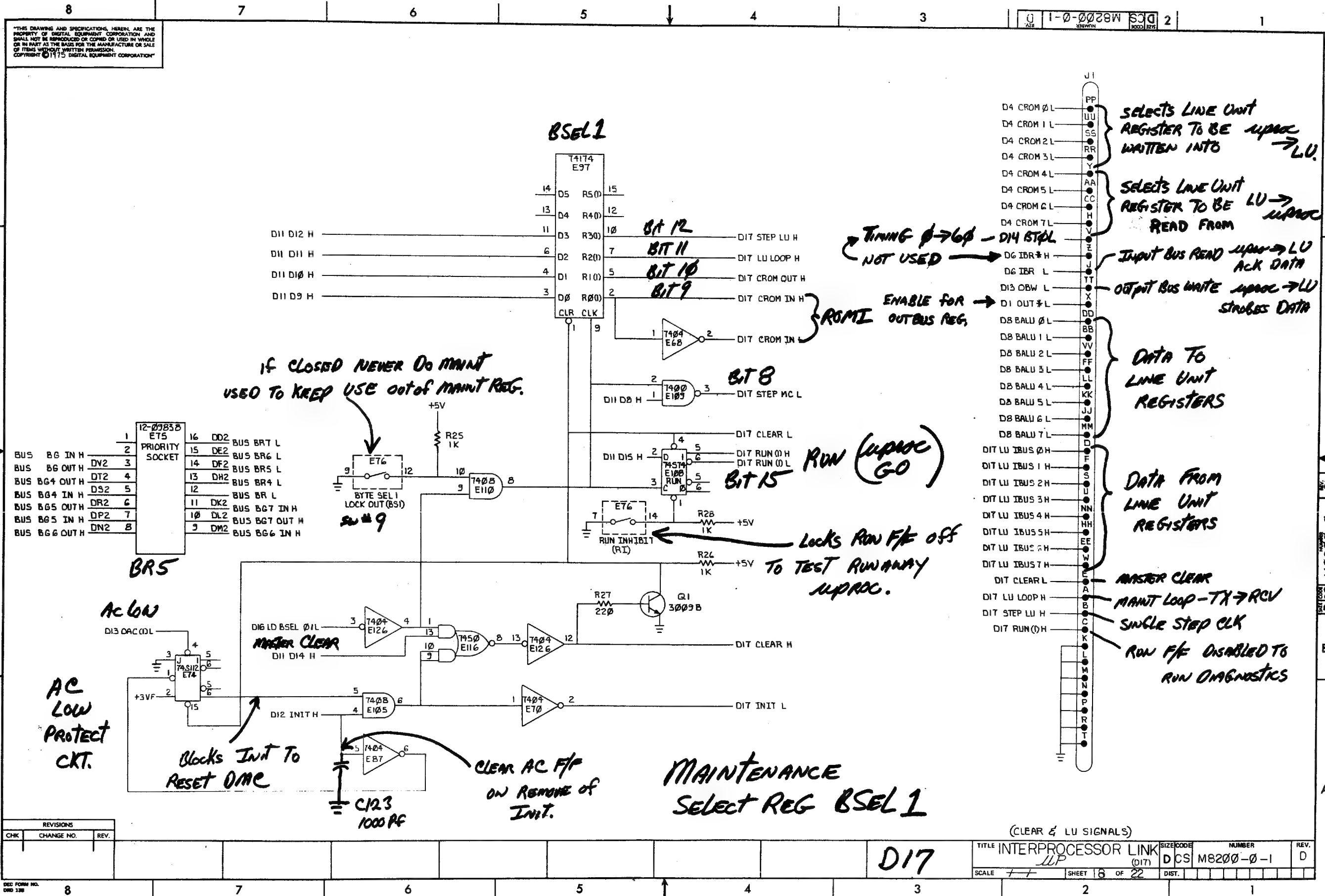
MAKES SURE "RUN SYNC" STAYS ON DURING "STEP" OPERATION SO THAT T₀ CAN BE CLEARED AT T₆₀

* NOT USED IN DMC APPLICATION ONLY M8200-YC



NOTE: POINT (A) IS FOR D14 TIMING PURPOSES ONLY.

REVISIONS			TITLE			NUMBER			REV.		
CHK	CHANGE NO.	REV.	D14			M8200-0-1			D		
			INTERPROCESSOR LINK			DCS			SHEET 15 OF 22		
			SCALE			DIST.					



DMC-17

➤ IS J1 CONNECTOR PINNING ONLY

[illegible]

REVISIONS		
CHK	CHANGE NO.	REV.

(INTERCONNECT LIST)

TITLE INTERPROCESSOR LINK		SIZE CODE	NUMBER	REV.
<i>ILP</i> (P18)		D CS	M 8200-0-1	H
SCALE <i>++</i>	SHEET 20 OF 22	DIST.		

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➤ IS J1 CONNECTOR PINNING ONLY

- DRAWING NUMBERS AND NUMBER OF CONN'S

[illegible]

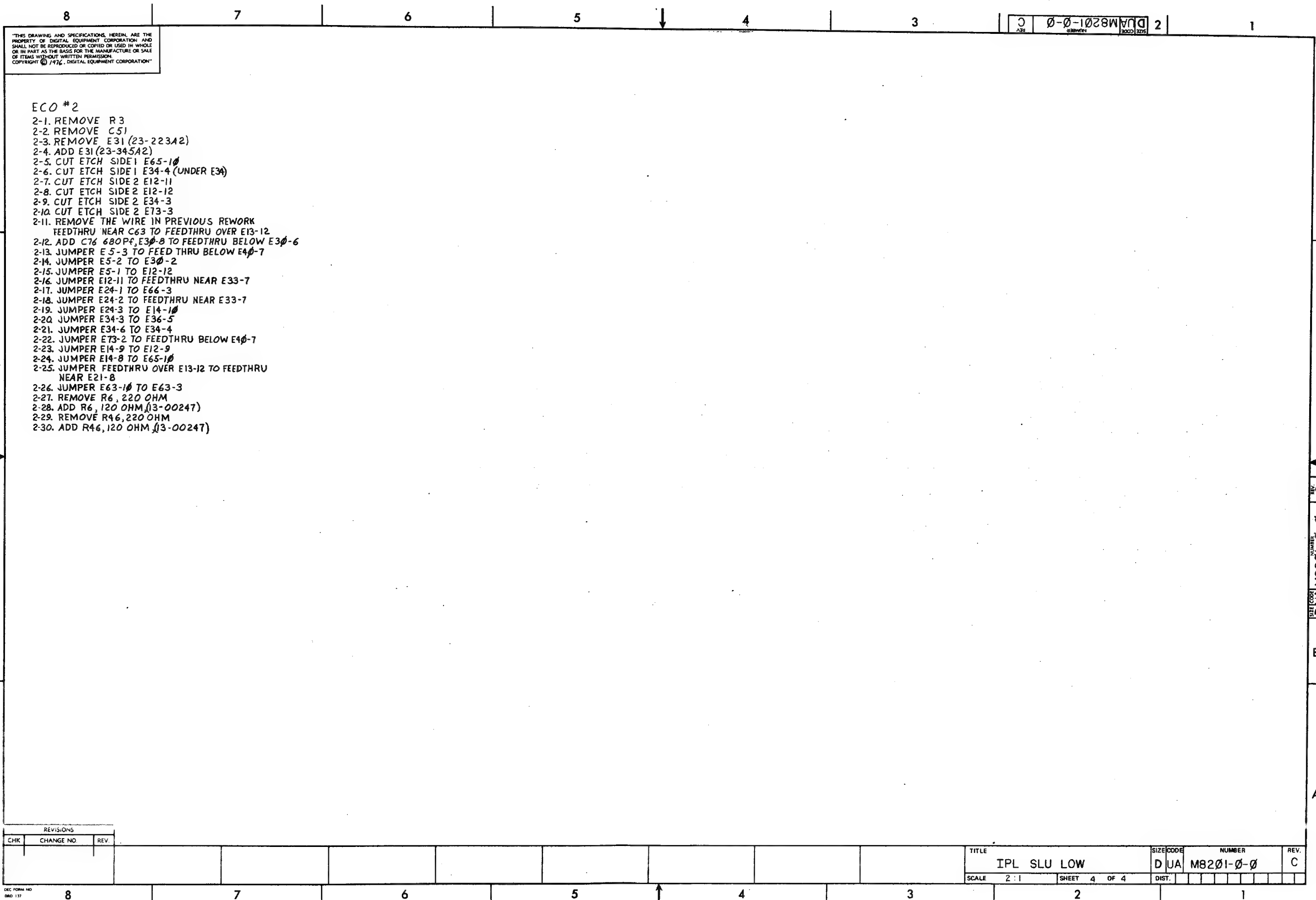
REVISIONS		
CH.	CHANGE NO.	REV.

(INTERCONNECT LIST)									
TITLE				SIZE	CODE	NUMBER		REV.	
INTERPROCESSOR LINK LLP (019)				D	CS	MB200-0-1		H	
SCALE		SHEET		OF		DIST.			
++		2		22					

DIGITAL EQUIPMENT CORPORATION				QUANTITY / VARIATION												NOTES:				
PARTS LIST																				
MADE BY R. KOPPENAL		CHECKED K. GLEEZEN		SECTION		M8200	M8200-YA	M8200-YB												
DATE 12-5-75		DATE 1-27-76		1																
ENG R. Luce		PROD R. Wall		ISSUED SECTION																
DATE 3-12-76		DATE 3-12-76		1																
ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION															REF DESIGNATION		
1	D-CS-M8200-0-1		CIRCUIT SCHEMATIC	REF	REF	REF														
2	K-CO-M8200-0-4		X-Y COORDINATE HOLE LOCATION	REF	REF	REF														
3	D-AH-M8200-0-5		ASSY/DRILLING HOLE LAYOUT	REF	REF	REF														
4	B-MH-M8200-0-6		MODULE ECO HISTORY	REF	REF	REF														
5		5011842	ETCHED CIRCUIT BOARD	1	1	1														
6		1000023	CAP, 330 Pf, 100V	1	1	1												C112,		
7		1001610	CAP, .01 UF, 50V	102	102	102												C1-C61, C63-C69, C100-C102, C71-C92, C94-C98, C104-C106, C103		
8		1000076	CAP, 39 Uf, 10V	3	3	3												C107, C108, C109		
9		1000042	CAP, 1000 Pf, 100V	4	4	4												C110, C111, C113, C123		
10		1004813	CAP, 10 Uf, 20V	7	7	7												C121, C114-C119		
11		1905547	IC, 7474	3	3	3												E117, E124, E129		
12		1905575	IC, 7400	2	2	2												E106, E109		
13		1905580	IC, 7450	2	2	2												E24, E116		
14		1909004	IC, 7402	2	2	2												E101, E115		
15		1909063	IC, 74H55	1	1	1												E91		
16		1909686	IC, 7404	4	4	4												E68, E70, E87, E126		
17		1909705	IC, 8881	4	4	4												E77, E78, E121, E123		
18		1909936	IC, 74151	9	9	9												E26, E57-E64		
19		1910087	IC, 4015	1	1	1												E72		
20		1910155	IC, 7408	5	5	5												E80, E105, E110, E114, E122		
21		1910409	IC, 74H103	1	1	1												E127		
ECO. NO.																				
00002																				
00003																				
00004																				
00005																				
00006																				
00007																				
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				INTERPROCESSOR LINK UP				D-11A-M8200-0-0				B		PL		M8200-0-0		J		
								SHEET 1 OF 5				INSERTION PARTS LIST DATA BASE REV C								

FN-01140A-16 (2/6/72b) DRB 125

Dmc-21



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ECO #2

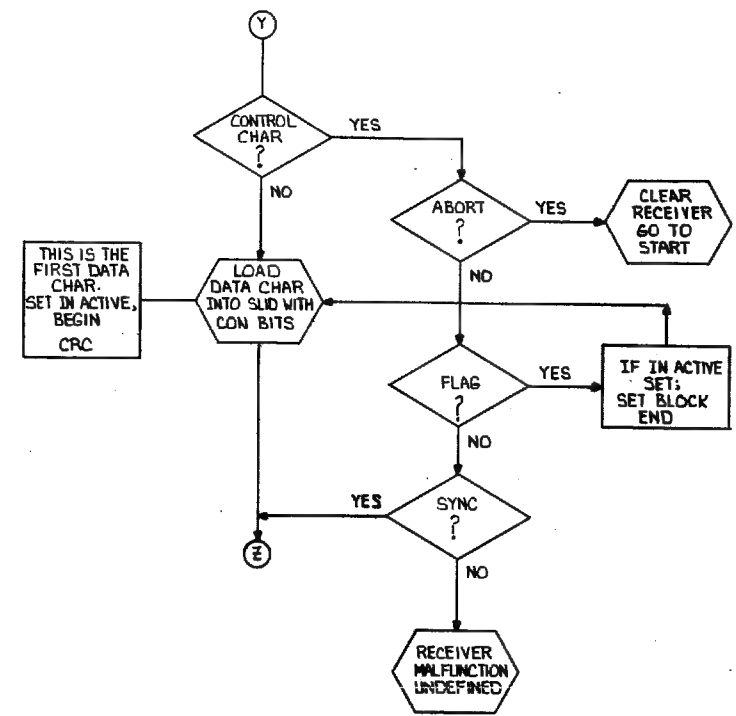
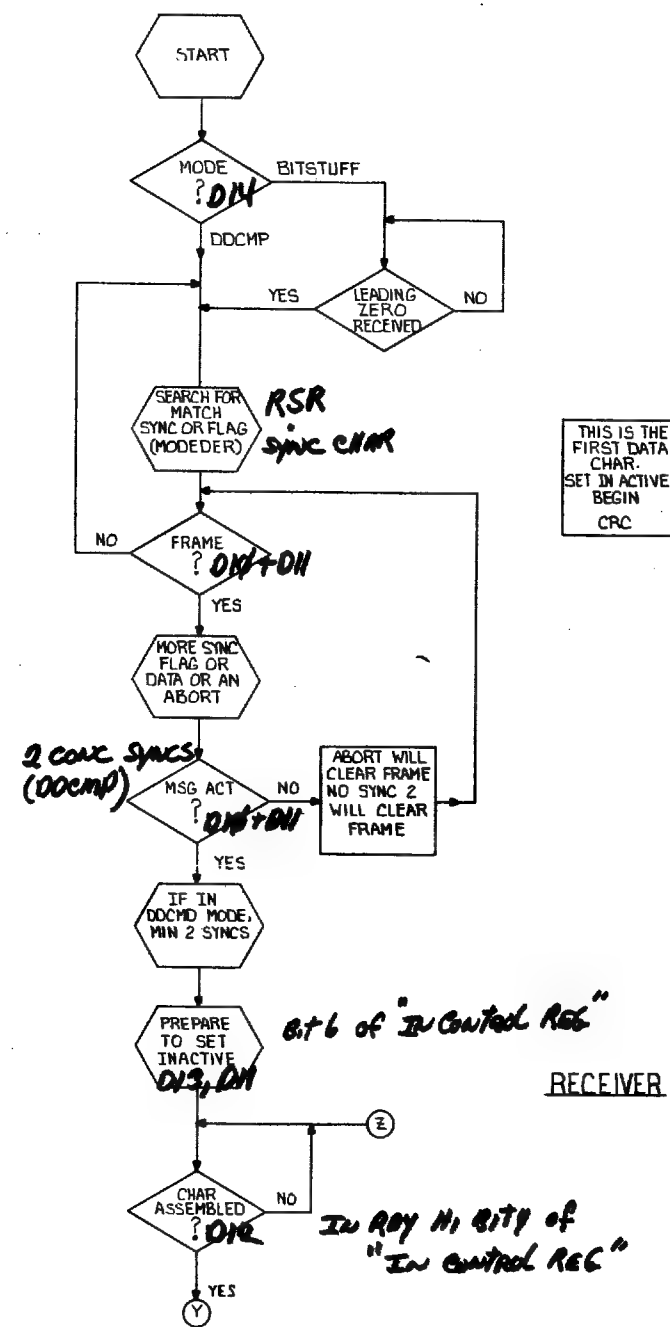
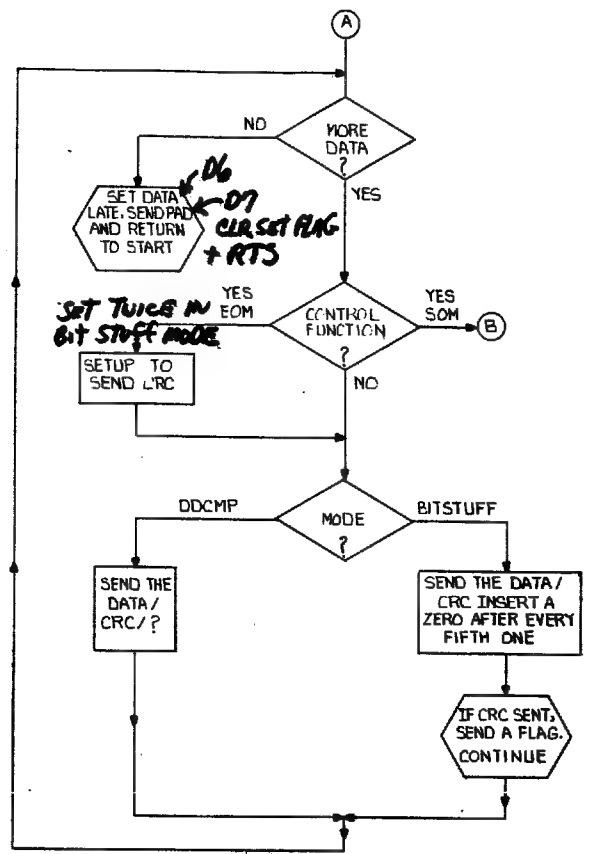
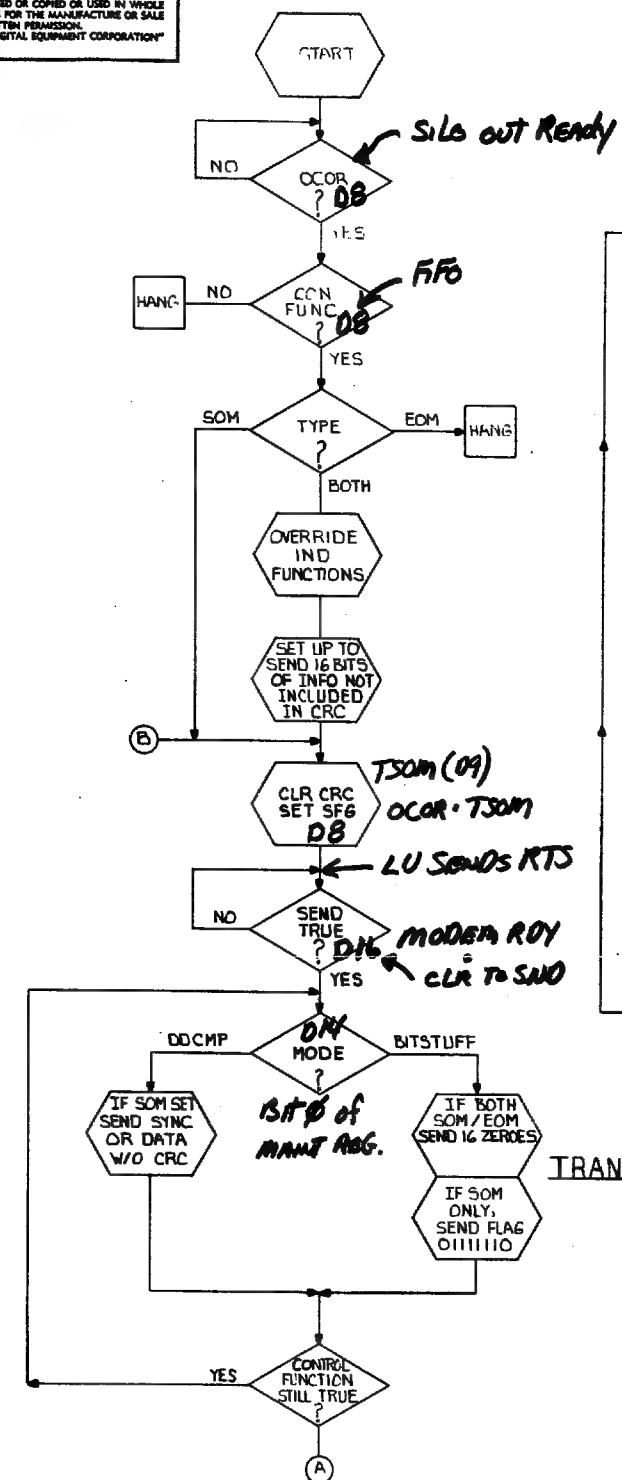
- 2-1. REMOVE R3
- 2-2. REMOVE C51
- 2-3. REMOVE E31 (23-223A2)
- 2-4. ADD E31 (23-345A2)
- 2-5. CUT ETCH SIDE 1 E65-10
- 2-6. CUT ETCH SIDE 1 E34-4 (UNDER E34)
- 2-7. CUT ETCH SIDE 2 E12-11
- 2-8. CUT ETCH SIDE 2 E12-12
- 2-9. CUT ETCH SIDE 2 E34-3
- 2-10. CUT ETCH SIDE 2 E73-3
- 2-11. REMOVE THE WIRE IN PREVIOUS REWORK
FEEDTHRU NEAR C63 TO FEEDTHRU OVER E13-12
- 2-12. ADD C76 680PF, E30-8 TO FEEDTHRU BELOW E30-6
- 2-13. JUMPER E5-3 TO FEEDTHRU BELOW E40-7
- 2-14. JUMPER E5-2 TO E30-2
- 2-15. JUMPER E5-1 TO E12-12
- 2-16. JUMPER E12-11 TO FEEDTHRU NEAR E33-7
- 2-17. JUMPER E24-1 TO E66-3
- 2-18. JUMPER E24-2 TO FEEDTHRU NEAR E33-7
- 2-19. JUMPER E24-3 TO E14-10
- 2-20. JUMPER E34-3 TO E36-5
- 2-21. JUMPER E34-6 TO E34-4
- 2-22. JUMPER E73-2 TO FEEDTHRU BELOW E40-7
- 2-23. JUMPER E14-9 TO E12-9
- 2-24. JUMPER E14-8 TO E65-10
- 2-25. JUMPER FEEDTHRU OVER E13-12 TO FEEDTHRU
NEAR E21-8
- 2-26. JUMPER E63-10 TO E63-3
- 2-27. REMOVE R6, 220 OHM
- 2-28. ADD R6, 120 OHM (13-00247)
- 2-29. REMOVE R46, 220 OHM
- 2-30. ADD R46, 120 OHM (13-00247)

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE IPL SLU LOW						SIZE CODE D UA		NUMBER M8201-0-0		REV. C
SCALE 2:1		SHEET 4 OF 4		DIST.						

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1-0-1028W
CS M8201-0-1
2



REV.	CHG.	NO.	DATE	BY	CHK.
1	C	00002	27 AUG 76	B. SMITH	
2	C	00003	17 SEP 76	B. SMITH	

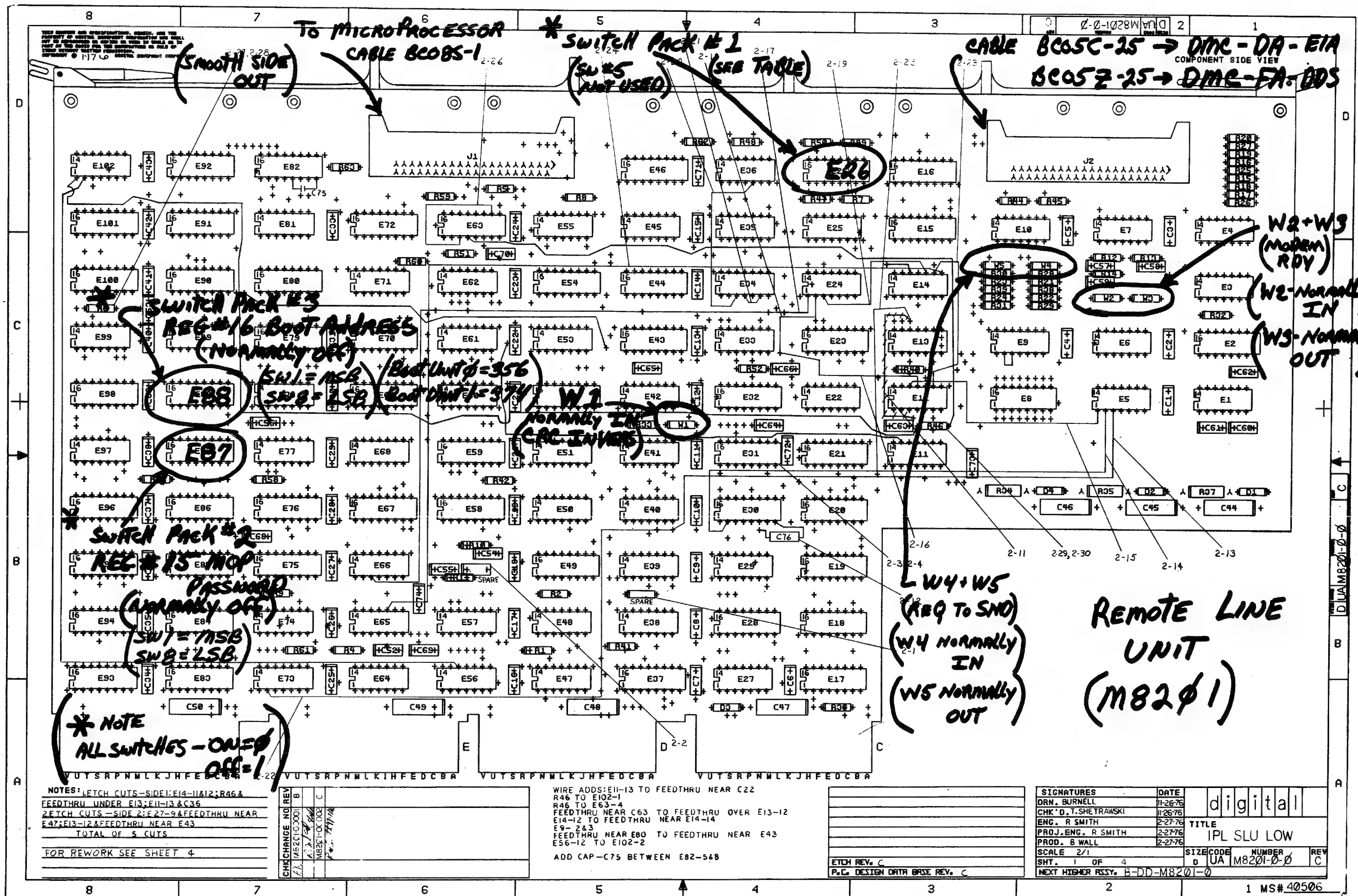
DRN R. W. W. 1-12-76		FIRST USED ON		digital
CHKD. 2-27-76		TITLE		IPL SLU LOW
ENG. 1. 2-27-76		NUMBER		D CS M8201-0-1
PROJ. ENG. 2-27-76		REV.		C
PROD. R. W. 2-27-76		SCALE		++
NEXT HIGHER ASSY.		SHEET		OF 16

DMC-23

DIGITAL EQUIPMENT CORPORATION																			
PARTS LIST																			
MADE BY R. KOPPENAL DATE 12-5-75		CHECKED K. GLEEZEN DATE 1-27-76		SECTION 1															
ENG R. LISEE DATE 3-12-76		PROD R. WALL DATE 3-12-76		ISSUED SECTION 1															
ITEM NO.		DRAWING NO.	PART NO.	DESCRIPTION	M8200	M8200-YA	M8200-YB												
83			23414A9	CROM,512-1023 Bits 4-7	-	1	-										E6		
84			23415A9	CROM,512-1023 Bits 8-11	-	1	-										E7		
85			23416A9	CROM,512-1023 Bits 0-3	-	1	-										E8		
86			23417A9	CROM,512-1023 Bits 12-15	-	1	-										E9		
87			23418A9	CROM,0-511 Bits 4-7	-	1	-										E14		
88			23419A9	CROM,0-511 Bits 8-11	-	1	-										E15		
89			23420A9	CROM,0-511 Bits 0-3	-	1	-										E16		
90			23421A9	CROM,0-511 Bits 12-15	-	1	-										E17		
			PRIOR TO ECO #10																

E.C.O. NO.															
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OMC-24



DMC-25

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SIGNAL NAME	3	4	5	6	7	8	9	10	11	12	13	14	15	16
D3 CROM 0 L (J2 - PP)	X	X												
D3 CROM 1 L (J2 - UU)	X	X												
D3 CROM 2 L (J2 - SS)	X	X												
D3 CROM 3 L (J2 - RR)	X	X												
D3 CROM 4 L (J2 - Y)	X	X												
D3 CROM 5 L (J2 - AA)	X	X												
D3 CROM 6 L (J2 - CC)	X	X												
D3 CROM 7 L (J2 - H)	X	X												
D3 RT 240 L (J2 - V)	X													
D3 IRR * H (J2 - Z)	X													
D3 IRR L (J2 - J)	X	X												
D3 QRR L (J2 - TT)	X	X												
D3 OUT * L (J2 - X)	X	X												
D3 BALU 0 L (J2 - DD)	X	X												
D3 BALU 1 L (J2 - BB)	X	X												
D3 BALU 2 L (J2 - VV)	X	X												
D3 BALU 3 L (J2 - FF)	X	X												
D3 BALU 4 L (J2 - LL)	X	X												
D3 BALU 5 L (J2 - KK)	X	X												
D3 BALU 6 L (J2 - JJ)	X	X												
D3 BALU 7 L (J2 - MM)	X	X												
D3 CLEAR L (J2 - E)	X	X												
D3 LU LOOP H (J2 - A)	X													
D3 STEP LW H (J2 - B)	X													
D3 RUN (1) H (J2 - C)	X													
D4 ALU 7 L	X													
D4 ALU 6 H	X													
D4 ALU 5 H	X													
D4 ALU 4 H	X													
D4 ALU 0 H	X													
D4 R10 SEL L	X													
D4 R11 SEL L	X													
D4 R12 SEL L	X													
D4 R13 SEL L	X													
D4 TB 0 H	X													
D4 R17 SEL L	X													
D4 SYNC 7 H	X	X												
D4 SYNC 6 H	X	X												
D4 SYNC 5 H	X	X												
D4 SYNC 4 H	X	X												
D4 SYNC 3 H	X	X												
D4 SYNC 2 H	X	X												
D4 SYNC 1 H	X	X												
D4 SYNC 0 H	X	X												
D4 TB11 H	X													
D4 TB10 H	X													
D4 TB 9 H	X													
D4 TB 8 H	X													
D4 TB 7 H	X													
D4 TB 6 H	X													
D4 TB 5 H	X													
D4 TB 4 H	X													
D4 TB 3 H	X													
D4 TB 2 H	X													
D4 TB 1 H	X													
D5 RDACP	X													

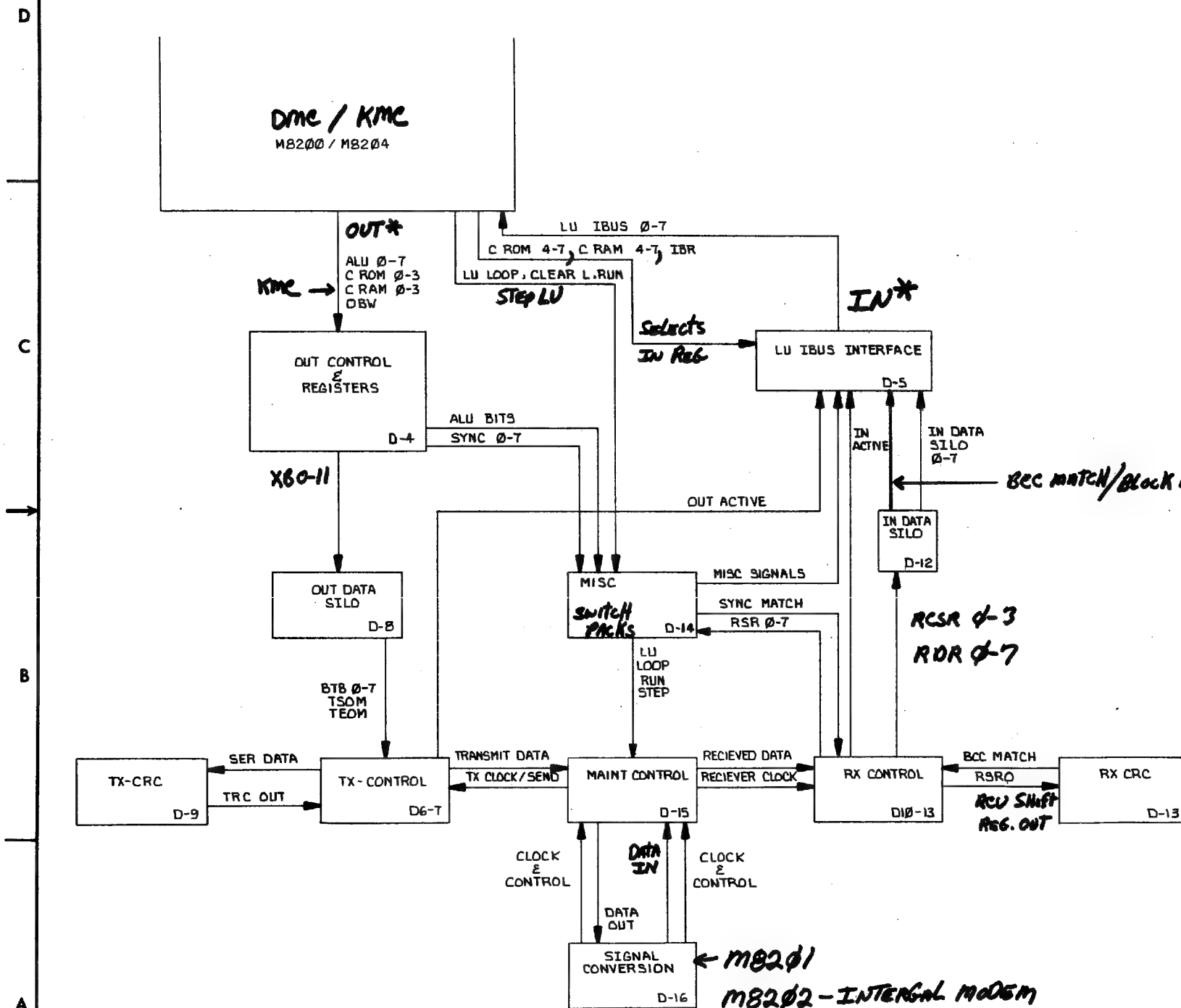
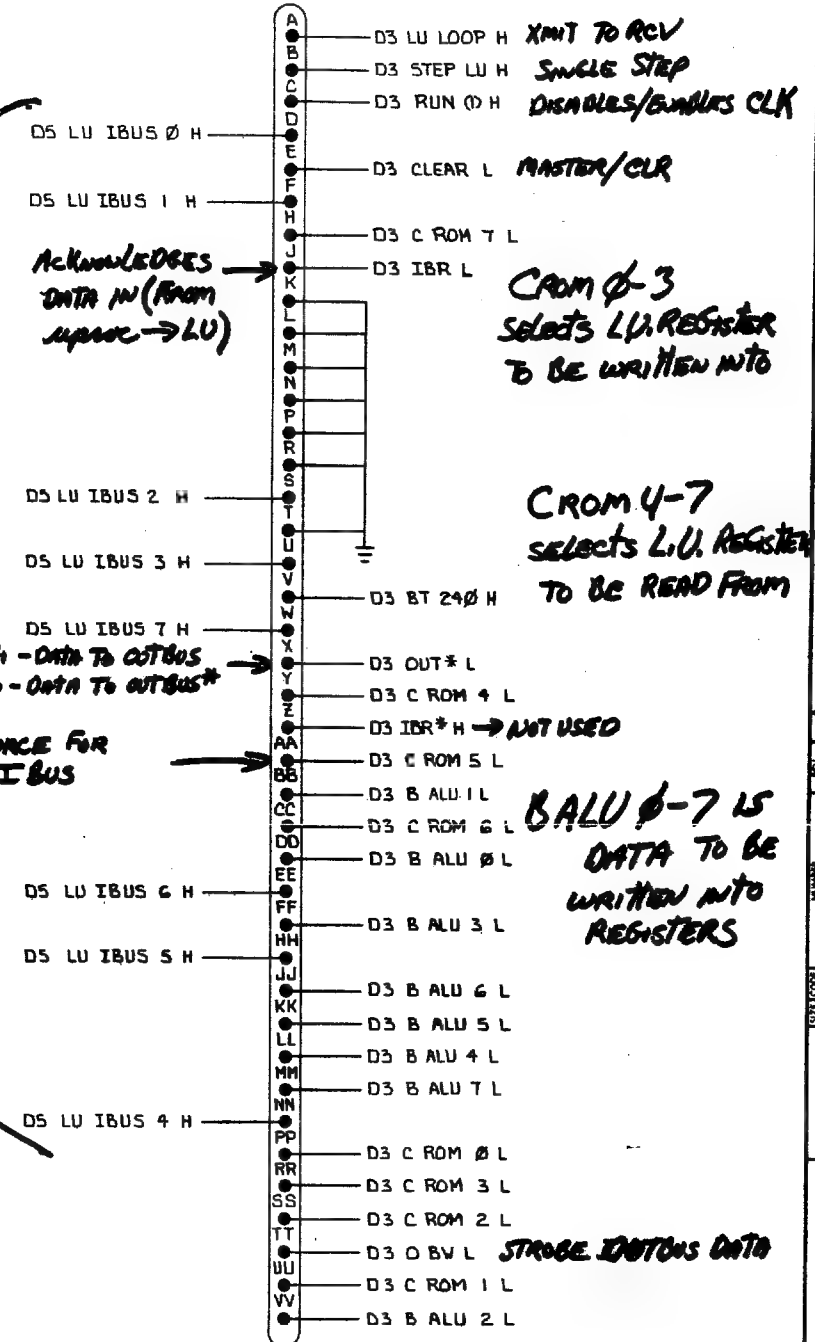
SIGNAL NAME	3	4	5	6	7	8	9	10	11	12	13	14	15	16
D5 LU BUS 0 H (J2 - D)	X	X												
D5 LU BUS 1 H (J2 - F)	X	X												
D5 LU BUS 2 H (J2 - S)	X	X												
D5 LU BUS 3 H (J2 - U)	X	X												
D5 LU BUS 4 H (J2 - NN)	X	X												
D5 LU BUS 5 H (J2 - HH)	X	X												
D5 LU BUS 6 H (J2 - EE)	X	X												
D5 LU BUS 7 H (J2 - W)	X	X												
D6 DP H					X		X							
D6 DLO H					X									
D6 EN TX DONE H					X	X								
D6 SABORT H					X	X	X							
D6 SELG H					X	X								
D6 SCRC H					X	X	X							
D6 TX CLK L					X	X								
D6 TCP H					X	X								
D6 SSOM H					X	X	X							
D6 BOCLRP L					X	X	X							
D6 SACT L					X	X	X							
D6 OUT ACTIVE H					X	X								
D6 DP L					X	X								
D7 CRC CLK EN H					X		X							
D7 SB LOAD H					X	X	X							
D7 EN TXSR H					X		X							
D7 LOAD L					X	X								
D7 LOAD H					X	X								
D7 TX SER OUT H					X		X							
D7 TCSC MAX H					X									
D7 TOLS					X									
D7 TDO					X									
D8 BTB 0 H					X	X								
D8 BTB 1 H					X	X								
D8 BTB 2 H					X	X								
D8 BTB 3 H					X	X								
D8 OOR H					X	X	X							
D8 OUT ROY H					X		X							
D8 SFG (1) H					X	X	X							
D8 TDM H					X		X							
D8 TDM H					X		X							
D8 BTB 7 H					X	X								
D8 BTB 6 H					X	X								
D8 BTB 5 H					X	X								
D8 BTB 4 H					X	X								
D8 COCLRP L					X		X							
D9 TCRC OUT H					X		X							
D9 QO H					X		X							
D10 EN FRAME H								X	X					
D10 EN INACT H								X	X					
D10 EN MSG ACT H								X	X					
D10 SI H					X			X	X					
D10 RCOD L								X	X					
D10 RSR → R BUF H								X	X					
D10 EN RSR H								X	X					
D10 FLG RECD H								X	X					
D10 ADDRS + SYNC RECD H								X	X					
D10 RCP L								X	X					

SIGNAL NAME	3	4	5	6	7	8	9	10	11	12	13	14	15	16
D10 RCOD H								X	X					
D11 RSR 0 H									X		X	X		
D11 RSR 1 H									X			X		
D11 FRAME (1) H									X					
D11 IN ACT (1) H				X					X	X		X		
D11 MSG ACT (1) H									X	X				
D11 RSR CLOCK									X			X		
D11 RSR MAX H									X	X				
D11 RSIP H									X	X				
D11 RDR 7 H									X	X				
D11 RDR 8 H									X	X				
D11 RDR 5 H									X	X				
D11 RDR 4 H									X	X				
D11 RDR 3 H									X	X				
D11 RDR 2 H									X	X				
D11 RDR 1 H									X	X				
D11 RDR 0 H									X	X				
D11 RDR 3 H									X	X				
D11 RDR 2 H									X	X				
D11 RDR 1 H									X	X				
D11 RDR 0 H									X	X				
D12 IN RDY H				X						X				
D12 IS 7 H				X						X				
D12 IS 6 H				X						X				
D12 IS 5 H				X						X				
D12 IS 4 H				X						X				
D12 IS 3 H				X						X				
D12 IS 2 H				X						X				
D12 IS 1 H				X						X				
D12 IS 0 H				X						X				
D12 INER 3 H				X						X				
D12 INER 2 H				X						X				
D12 BCC MATCH H				X						X				
D12 IN BLOCK END H				X						X				
D12 ICIR H				X						X	X			
D13 QI H				X								X		
D13 BCC STATE H									X		X			
D14 DDCMP H				X	X	X		X	X			X	X	
D14 ICURP H									X			X		
D14 ICURP L									X	X	X			
D14 OCURP H					X								X	
D14 OCURP L				X			X						X	X
D14 NO CRC					X								X	
D14 BIT STF H									X			X		
D14 GRTP					X	X			X			X		
D14 SEC MODE									X			X		
D14 SECURE H				X									X	
D14 SW H				X									X	
D14 ADDRS + SYNC H									X				X	
D14 REG 15-7 H					X							X		
D14 REG 15-6 H					X							X		
D14 REG 15-5 H					X							X		
D14 REG 15-4 H					X							X		
D14 REG 15-3 H					X							X		
D14 REG 15-2 H					X							X		
D14 REG 15-1 H					X							X		

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From DMC upproc to L.U.

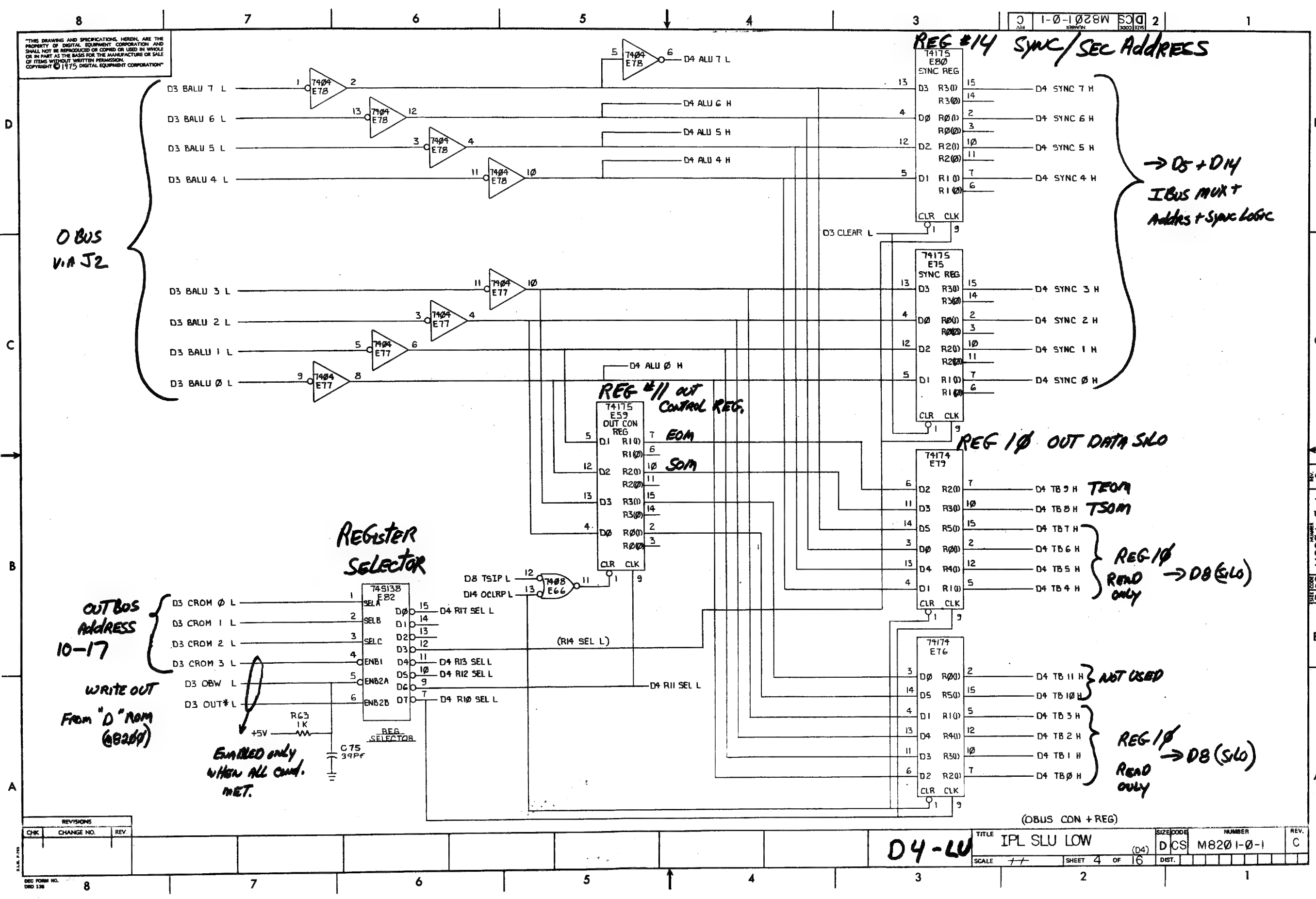
J2-CONNECTOR



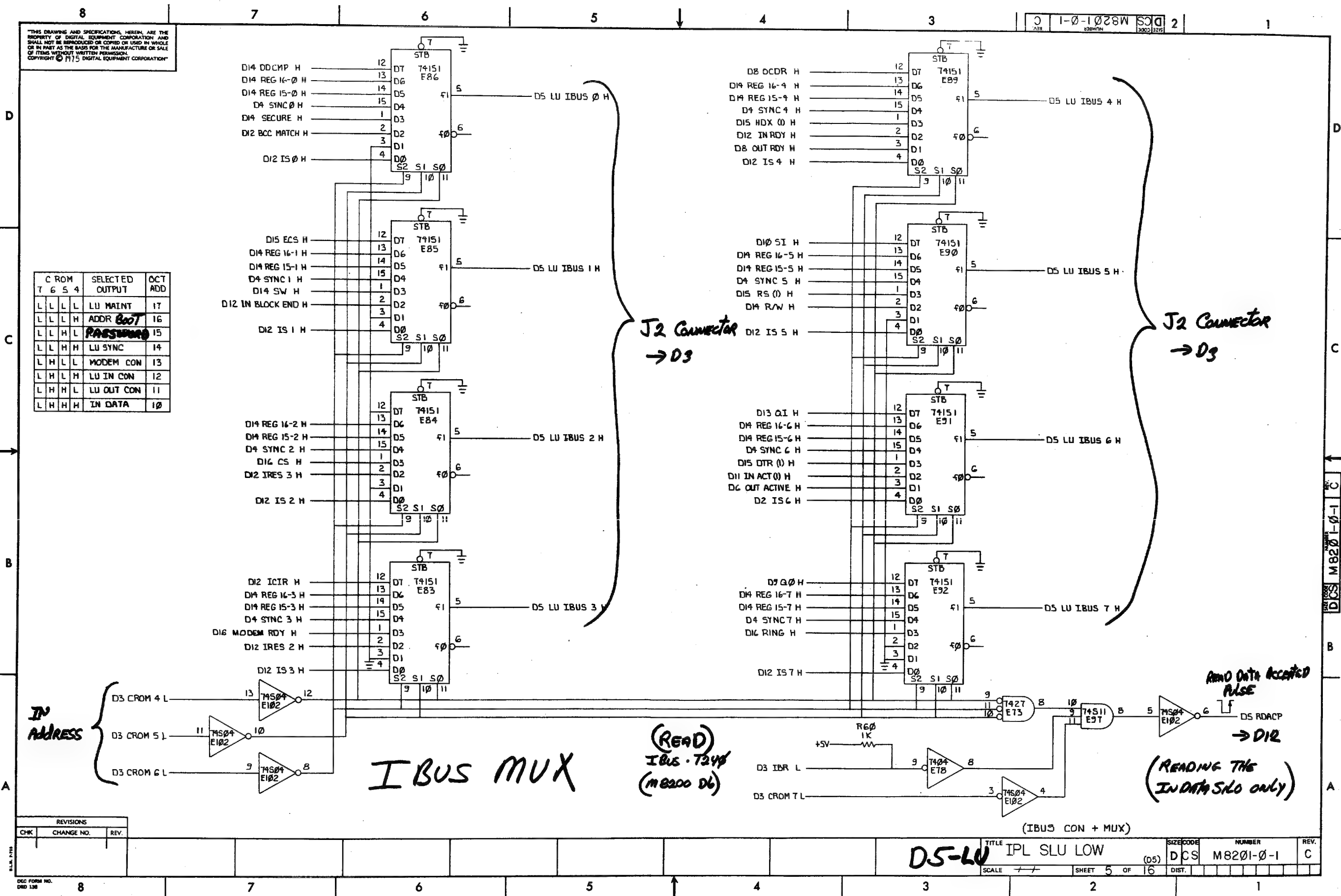
D3

CHK	CHANGE NO.	REV.	TITLE	SIZE CODE	NUMBER	REV.
			IPL SLU LOW	D C S	M8201-0-1	C
			SCALE	SHEET	3 OF 16	

DMC-27

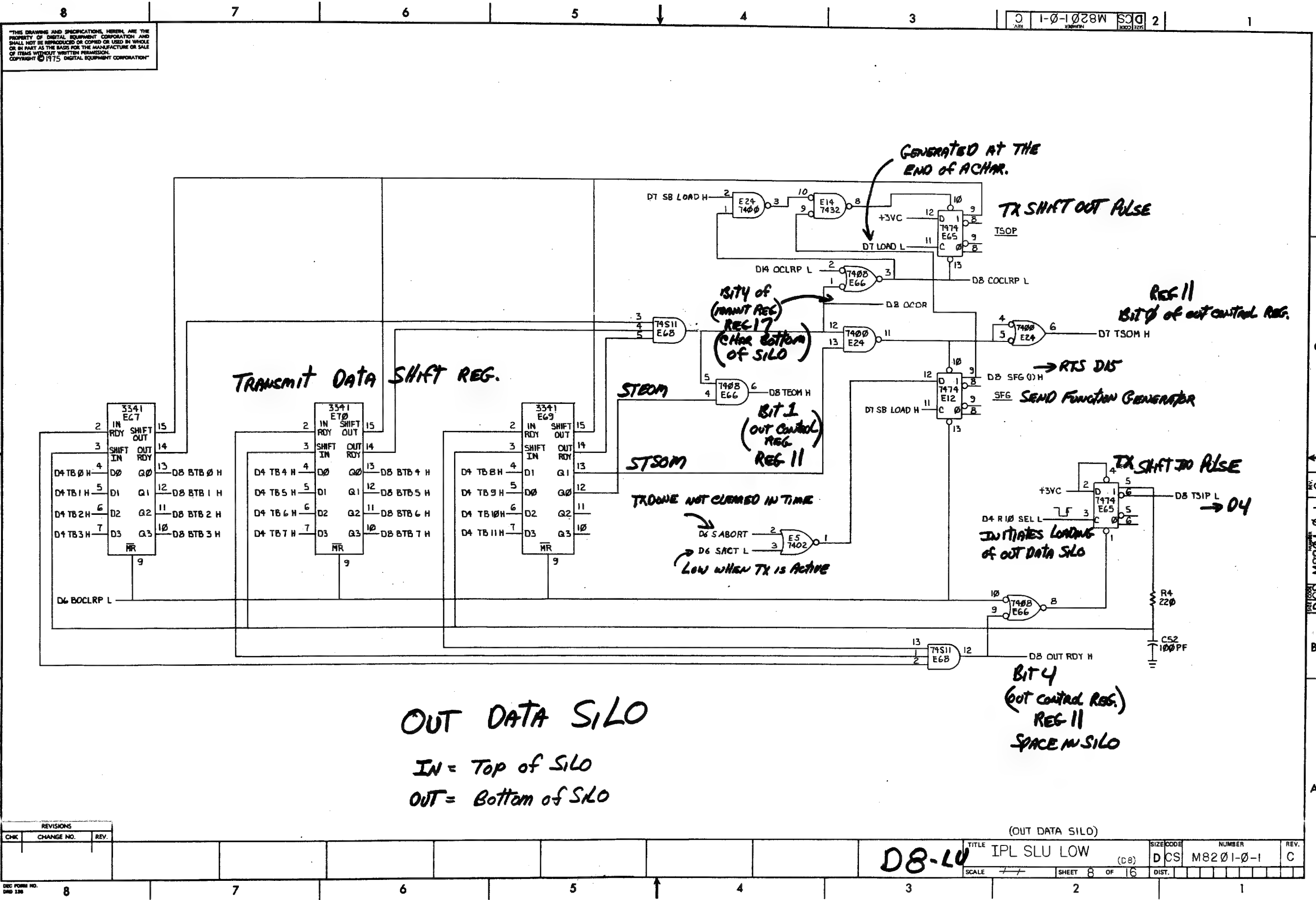


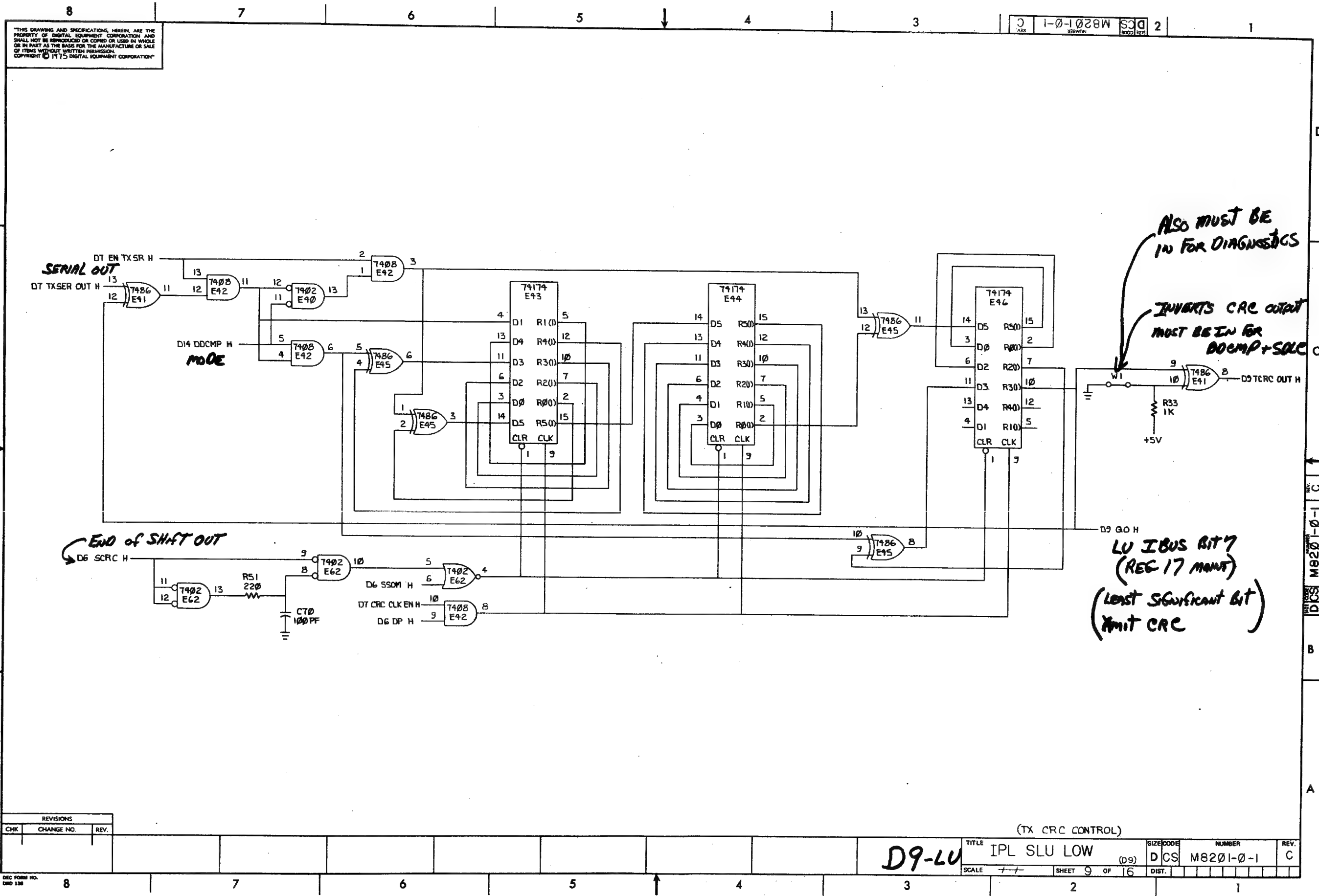
REVISIONS													(DBUS CON + REG)																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																			
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DMC-29







DMC-33





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1-0-1028W DCS M8201-0-1

XOR'S FOR CRC FOR
SDLC = 016417

Bit 6 of MAINT REG
(REG 17)
LEAST SIGNIFICANT BIT
of REV CRC
MODE BIT

ϕ = NO ERROR
DDCMP

ϕ = NO ERROR
SDLC

REV CRC CONTROL

(RX CRC CONTROL)

013-LU IPL SLU LOW

SIZE CODE DCS
NUMBER M8201-0-1

REV. C

SHEET 13 OF 16

DIST.

1

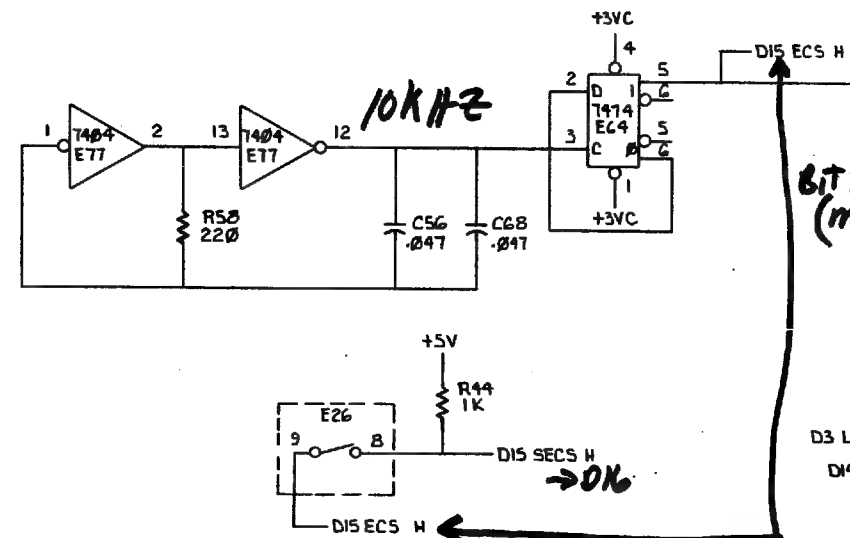
DMC-37



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ONLINE/LOOP MUX

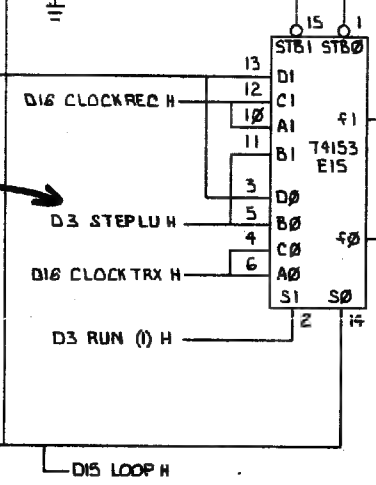
DT TDL5	L=1	13	B3	f3	12	L=1	DIS RX DATA
DIS RDATA	L=1	14	A3	f3	9	L=ON	DIS RTS
+3VA		10	B2	f2	7	H=ON	DIS SEND H → D6 (Function Mem)
DIS RS (I) L	L=ON	11	A2	f2	4	L=ON	DIS DTR
DIS RS (I) H	H=ON	6	B1	f1			
DIS SEND (I) H	H=ON	5	A1	f1			
+3VA		3	B0	f0			
DIS TR (I) L	L=ON	2	A0	f0			



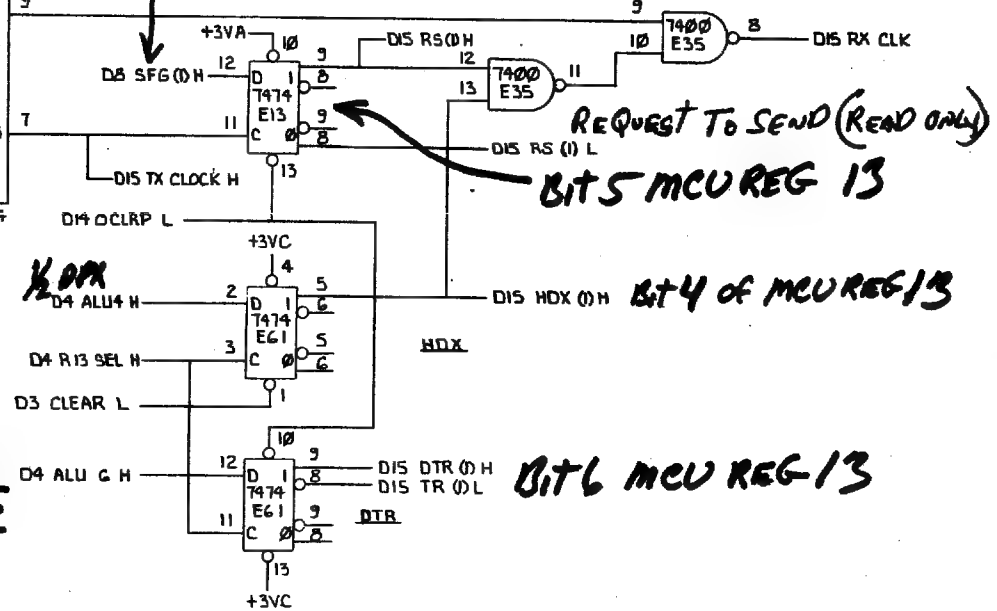
Sw Pack #1
Switch #8 SEND EXTERNAL
CLOCK SOURCE

BIT 2 REG 17
(MAINT.)

Run=S1	Loop=S0	Func.
1	0	A+C
1	1	D
0	1	B



SEND Function Generator



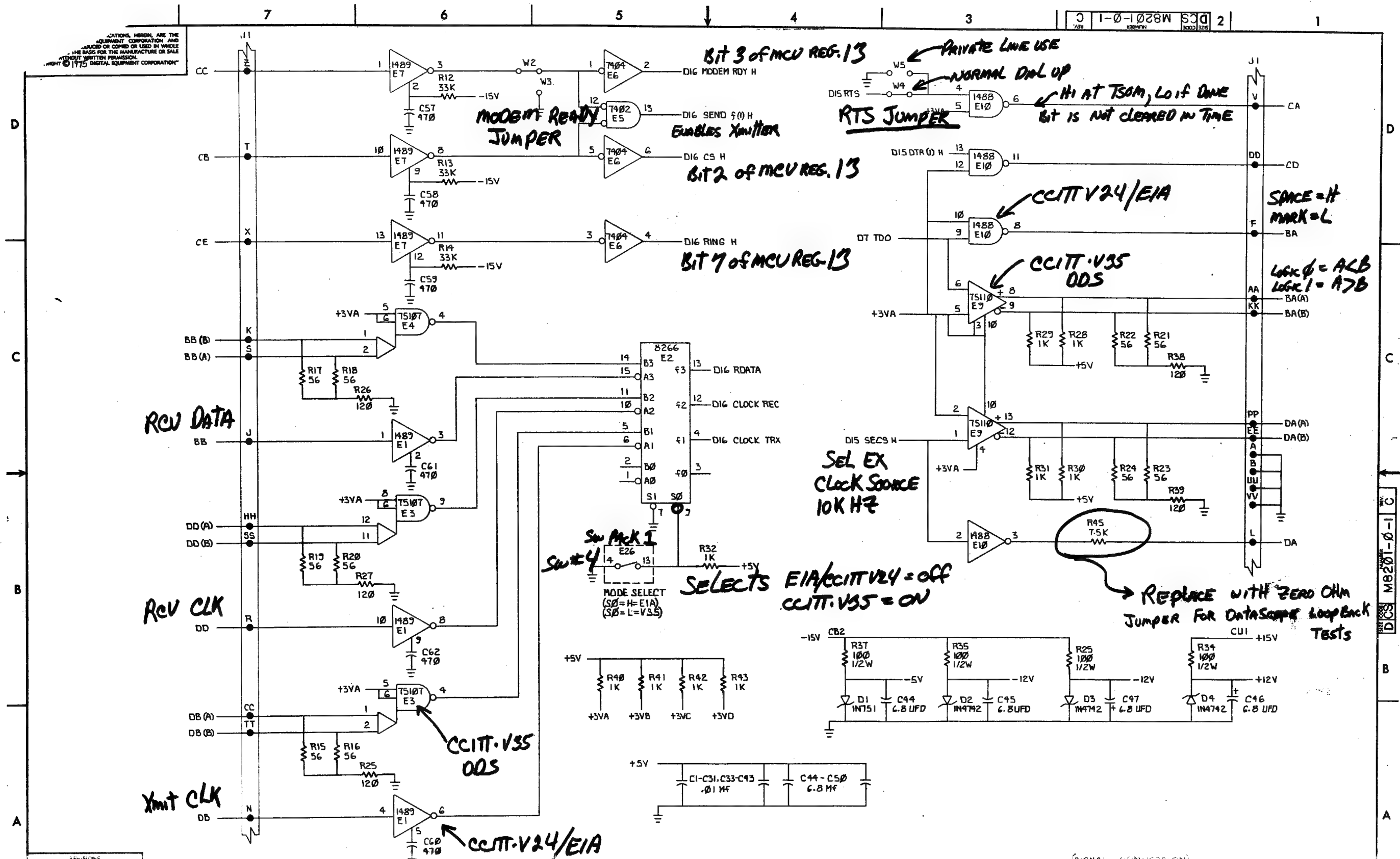
REQUEST TO SEND (READ ONLY)
BIT 5 MCU REG 13

BIT 4 of MCU REG 13

BIT 6 MCU REG 13

CHK	CHANGE NO.	REV.

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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		D16-LU		IPL SLU LOW (D16)		SIZE CODE		NUMBER		REV.	
SCALE		16		OF 16		DIST.		M8201-0-1		C	

DMC-40

DIGITAL EQUIPMENT CORPORATION				QUANTITY / VARIATION												NOTES: ** Any variation is acceptable			
PARTS LIST																			
MADE BY R. KOPPENAL		CHECKED KENT GLEEZEN		SECTION 1															
DATE 1-12-76		DATE 3-2-76																	
ENG R. M. SMITH		PROD R. WALL		ISSUED SECTION 1															
DATE 3-2-76		DATE 3-2-76																	
ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION															REF DESIGNATION	
1	D-MD-5011711	5011711	ETCHED CIRCUIT BOARD	1															
2		1000016	CAP, 100 Pf, 100V	5														C52-C55, C70	
3		1000024	CAP, 470 Pf, 100V	11														C57-C62, C64, C65, C69, C72, C73	
4		1001610 **	CAP, .01 Mf, 50V	43														C1-C31, C71, C33-C43	
5		1005306	CAP, 6.8 Mf, 35V	7														C44-C50	
6		1010978-32	CAP, .047 Mf, 50V	2														C56, C68	
7		1109502	DIODE, IN4742	3														D2-D4	
8		1110994	DIODE, IN751A	1														D1	
9		1905547	IC, 7474	9														E12, E13, E34, E39, E47, E61, E64, E65, E99	
10		1905575	IC, 7400	2														E35, E24	
11		1909004	IC, 7402	4														E62, E40, E60, E5	
12		1909686	IC, 7404	5														E6, E25, E36, E77, E78	
13		1909712	IC, 8242	6														E48, E51, E53, E55, E74, E81	
14		1909934	IC, 8266	1														E2	
15		1909936	IC, 74151	8														E83-E86, E89-E92	
16		1909937	IC, 74153	1														E15	
17		1910011	IC, 7486	3														E41, E45, E50	
18		1910041	IC, 74164	4														E71, E22, E27, E57	
19		1910155	IC, 7408	5														E33, E38, E42, E56, E66	
20		1910268	IC, 75107	2														E3, E4	
21		1910322	IC, 1488L	1														E10	
E.C.O. NO. M8201-1 M8201-2																			
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				SHEET 1 OF 3				INSERTION PARTS LIST DATA BASE REV C											

EN-01140A-16-R276(325)

DRB 125

Dmc-41

DIGITAL EQUIPMENT CORPORATION				QUANTITY / VARIATION												NOTES:			
PARTS LIST																			
MADE BY R. KOPPENAL		CHECKED KENT GLEEZEN		SECTION 1															
DATE 1-12-76		DATE 3-2-76																	
ENG R. M. SMITH		PROD R. WALL		ISSUED SECTION 1															
DATE 3-2-76		DATE 3-2-76																	
ITEM NO.	DRAWING NO.	PART NO.	DESCRIPTION													REF DESIGNATION			
42		1300247	RES, 120, 1/4W, 5%	7												R25-R27, R38, R39, R6, R46			
43		1302602	RES, 56, 1/4W, 5%	10												R15 - R24			
44		1311003-02	RES, PACK	3												E19, E32, E98			
45		1301422	RES, 7.5K, 1/4W, 5%	1												R45			
46		1000023	CAP, 330 Pf	1												C63			
47		1301874	RES, 5.6K, 1/4W, 5%	1												R5			
48		1302395	RES, 56K, 1/4W, 5%	1												R59			
49		1000010	CAP, 39 Pf	1												C75			
50		23-345A2	ROM 256 X 4 OC (T1)	1												E31			
51		23-220A2	ROM 256 X 4 OC (T3)	1												E21			
52		23-302A2	ROM 256 X 4 OC (T2)	1												E20			
53		23-303A2	ROM 256 X 4 OC (R1)	1												E28			
54		23-304A2	ROM 256 X 4 OC (R2)	1												E18			
55		1002424	CAP, 1200 Pf, 100V	1												C66			
56		9107256-10	CLEAR TUBING	A/R															
57		1000026	CAP, 680 Pf, 100V	2												C74, C76			
58		9009185	INSULATED JUMPERS	3												W1, W2, W4			
59		1209941-02	CONNECTOR	2												J1, J2			
60		1210711-02	HEX HANDLE	1															
61		9105740-55	30 AWG WIRE	A/R															
62		9006732	EYELETTES	12															
63		1211164-04	SWITCH PACK - 8 PER PACK	3												E26, E87, E88			

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EN-01140A-16-R276(325) DRB 125

TITLE

IPL SLU LOW

ASSY NO.

D-UA-M8201-0-0

SHEET 3 OF 3

SIZE CODE

B PL

NUMBER

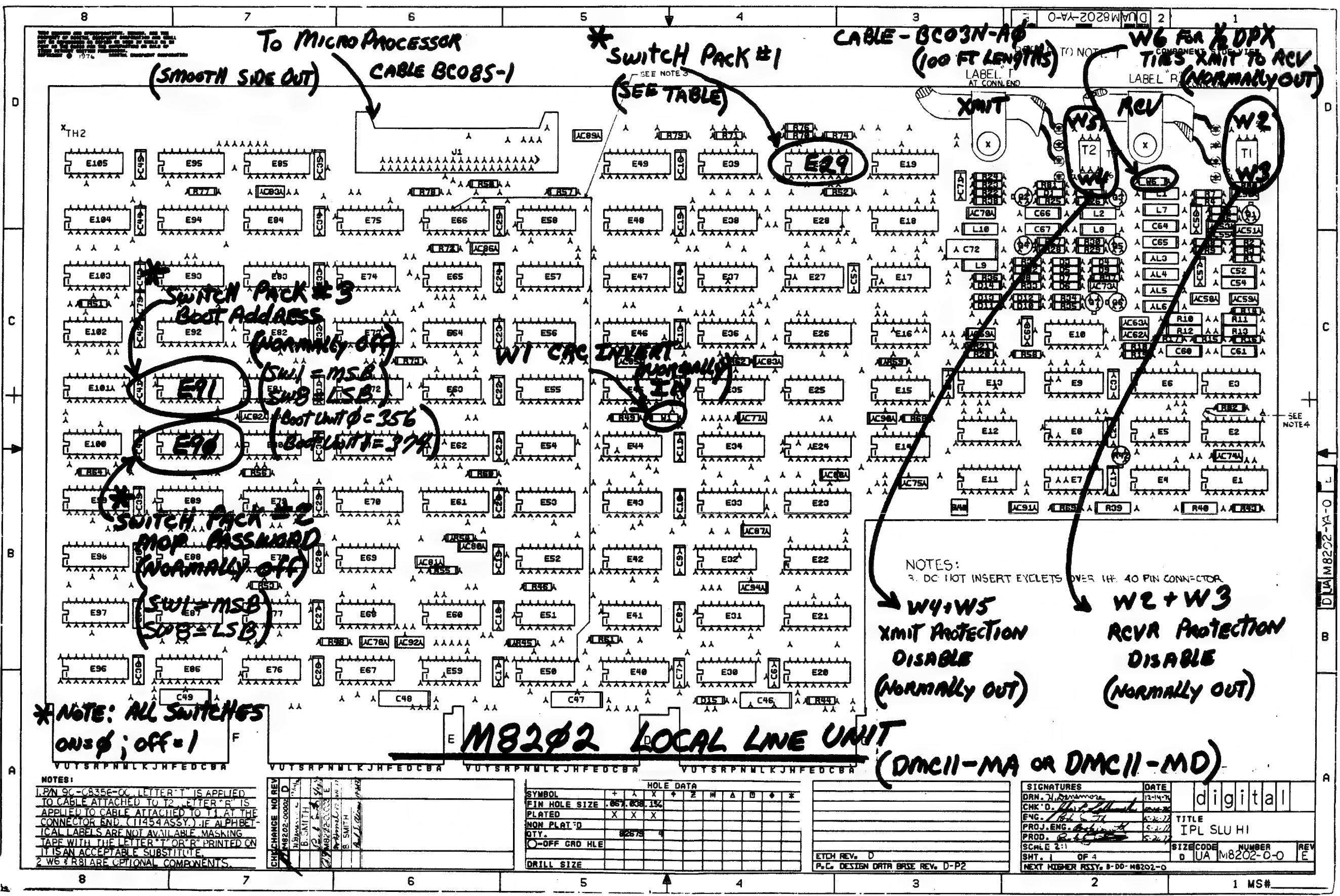
M8201-0-0

REV.

C

INSERTION PARTS LIST DATA BASE REV C

DMC-42



NOTES:
1. PN 9C-C8356-0C, LETTER "T" IS APPLIED TO CABLE ATTACHED TO T2. LETTER "R" IS APPLIED TO CABLE ATTACHED TO T1 AT THE CONNECTOR END (11454 ASSY). IF ALPHABETICAL LABELS ARE NOT AVAILABLE, MASKING TAPE WITH THE LETTER "T" OR "R" PRINTED ON IT IS AN ACCEPTABLE SUBSTITUTE.
2. W6 & R8 ARE OPTIONAL COMPONENTS.

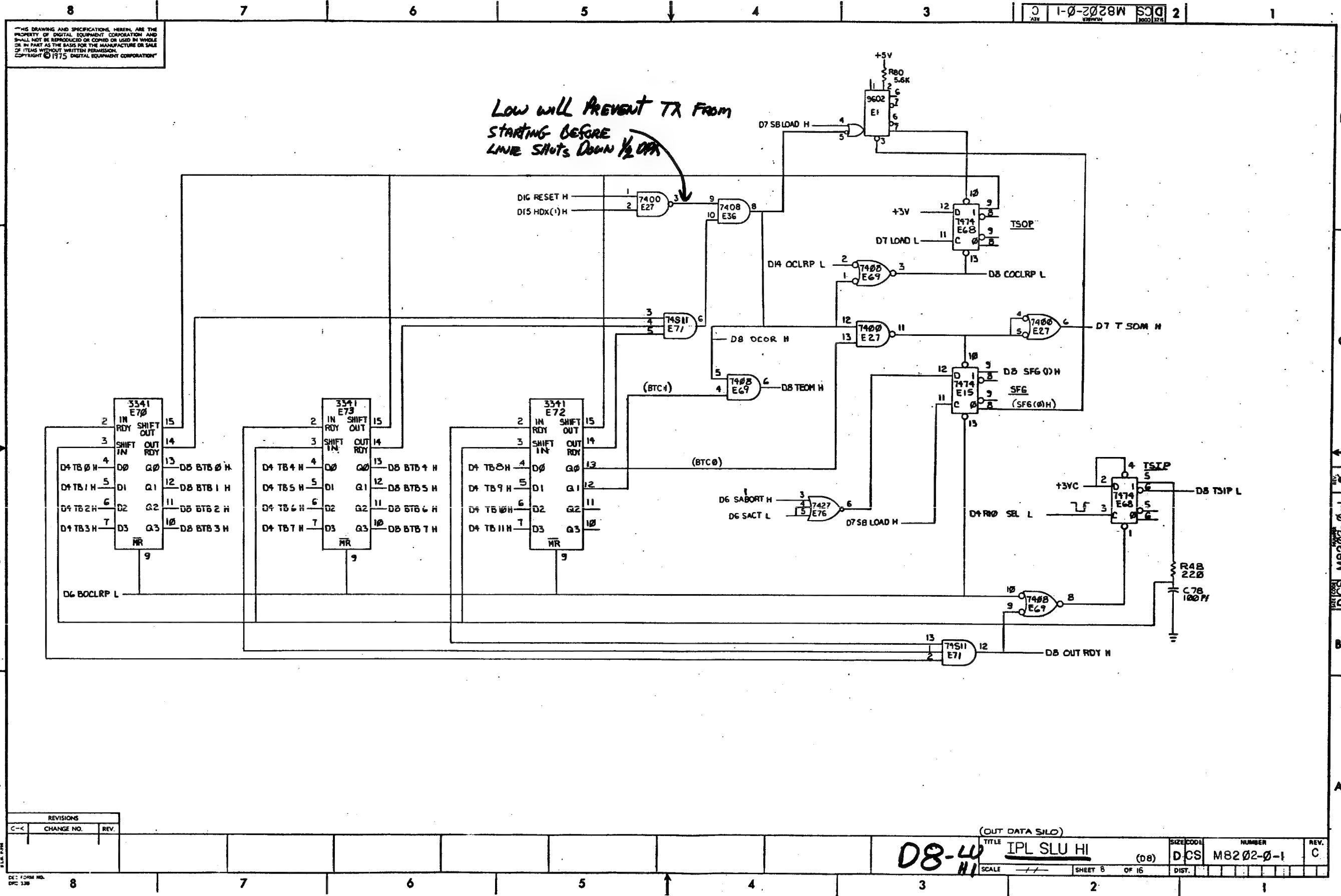
CH	REV	DATE	BY	APP
1	1	12-14-74	B. SMITH	
2	1	12-14-74	B. SMITH	

HOLE DATA	
SYMBOL	SIZE
FIN HOLE SIZE	.057, .038, .134
PLATED	X X X
NON PLAT'D	
QTY.	22875
Q-OFF CRD HLE	
DRILL SIZE	

ETCH REV. D
P.C. DESIGN DATA BASE REV. D-P2

SIGNATURES		DATE	digital	
DRN. J. B. B. B.		12-14-74		
CHK'D. J. B. B. B.				
ENG. J. B. B. B.		5-2-77	TITLE	
PROD. J. B. B. B.		5-2-77	IPL SLU HI	
SCALE 2:1			SIZE	CODE
SHT. 1 OF 4			D	UA
NEXT NUMBER ASSY. D-00-M8202-0			NUMBER	REV
			M8202-0-0	E

TITLE	digital
IPL SLU HI	
SIZE CODE	NUMBER
0 UA M8202-0-0	REV
1 MS#	

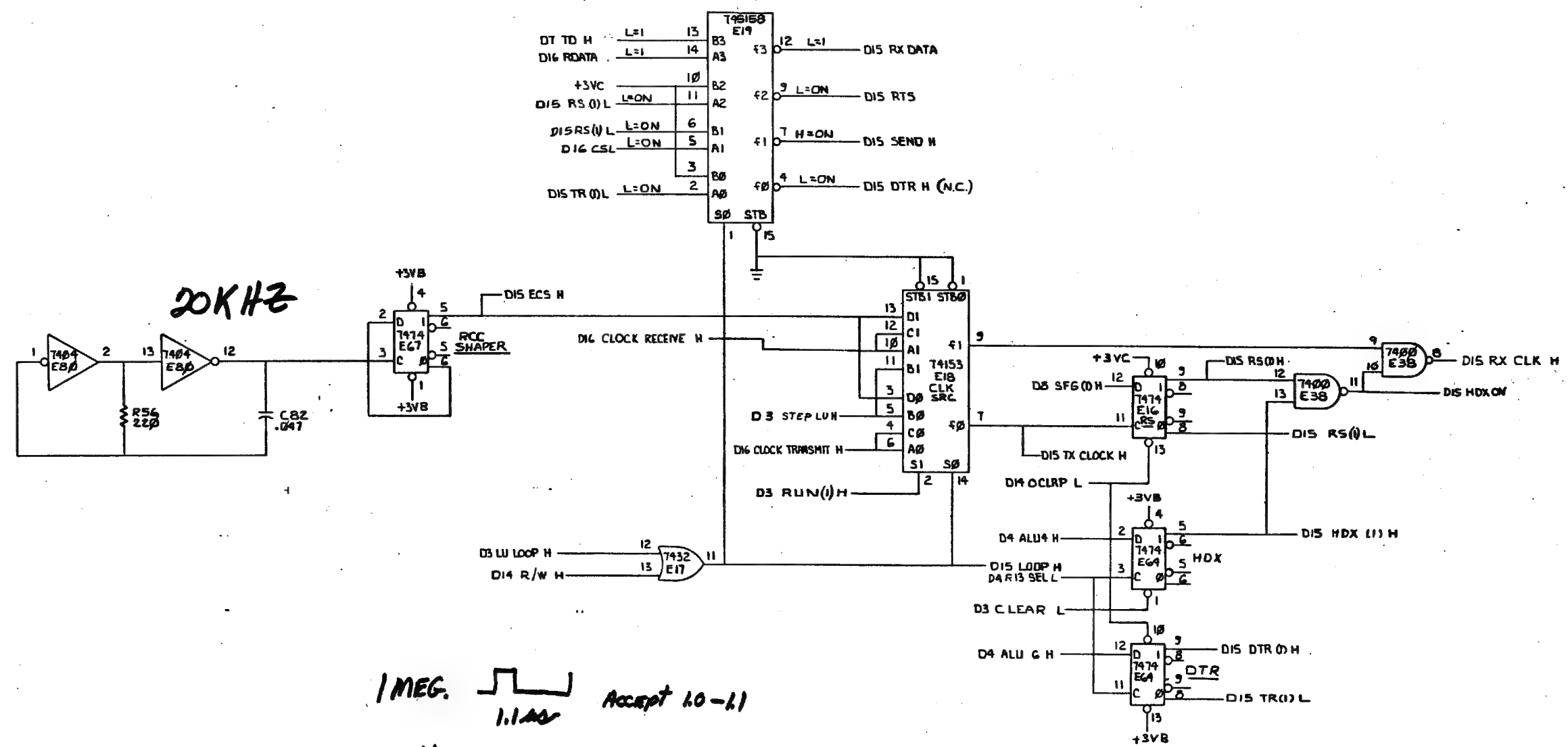


DMC-45

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1-0-2028W DCS M8202-0-1 2

ONLINE/LOOP MUX



1MEG. Accept 10-11
1.1us
56K Accept 17-18.6
17.8us
500K Accept 18-22
2us
250K Accept 26-24
4us

REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO. 720 128

D15-LU HI

(MAINT / MODEM CONTROL)

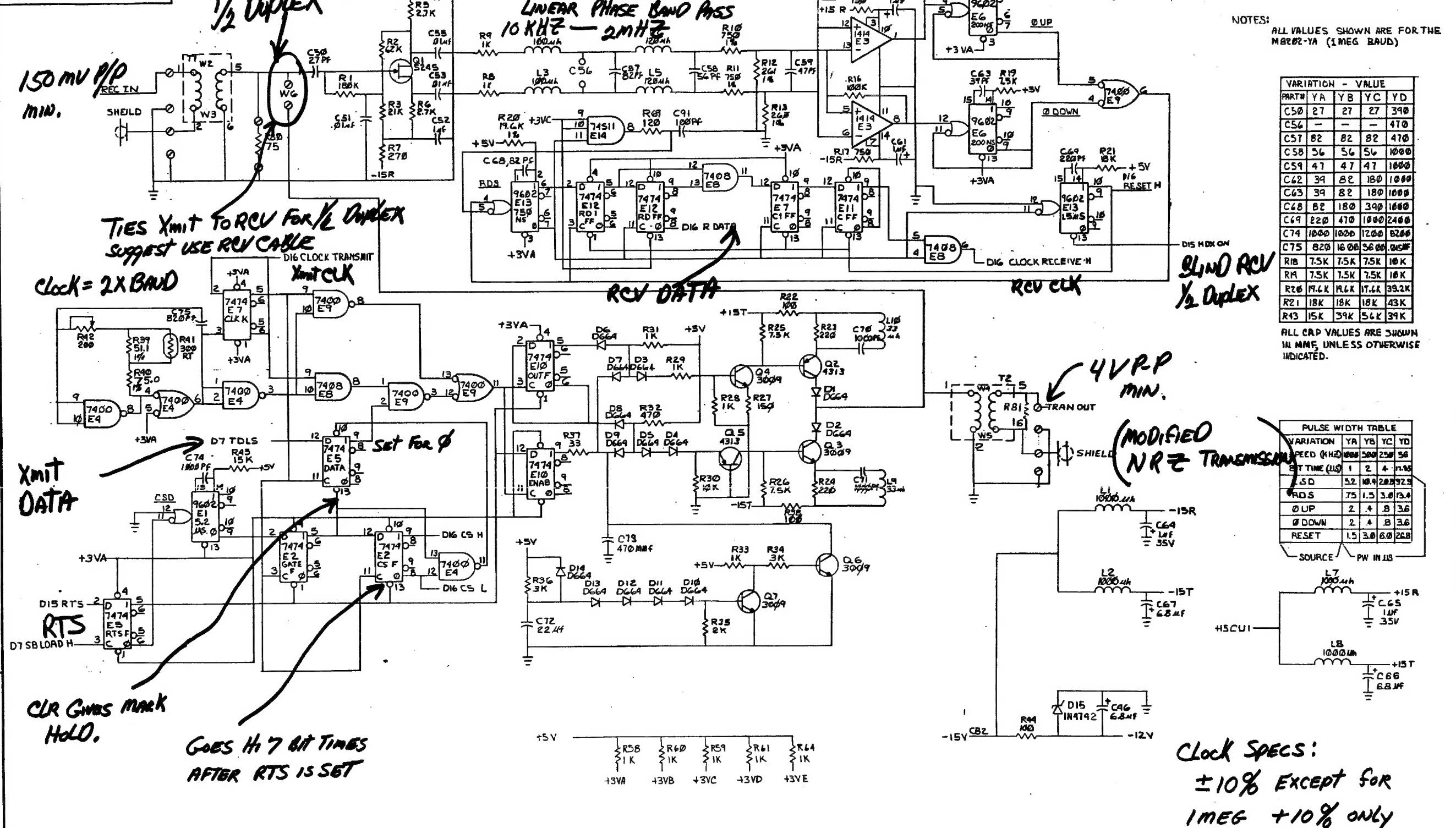
TITLE IPL SLU HI

SCALE 1/1 SHEET 15 OF 16

SIZE CODE	NUMBER	REV.
DCS	M8202-0-1	C

DMC-46

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NOTES: ALL VALUES SHOWN ARE FOR THE M8202-YA (1MEG BAUD)

VARIATION - VALUE				
PART#	YA	YB	YC	YD
C50	27	27	27	390
C56	-	-	-	470
C57	82	82	82	470
C58	56	56	56	1000
C59	47	47	47	1000
C62	39	82	180	1000
C63	39	82	180	1000
C68	82	180	390	1000
C69	820	470	1000	2400
C74	1000	1000	1200	8200
C75	820	1600	5600	0.05M
R18	7.5K	7.5K	7.5K	10K
R19	7.5K	7.5K	7.5K	10K
R20	17.4K	17.4K	17.4K	39.2K
R21	18K	18K	18K	43K
R43	15K	39K	56K	39K

ALL CAP VALUES ARE SHOWN IN MME, UNLESS OTHERWISE INDICATED.

PULSE WIDTH TABLE				
VARIATION	YA	YB	YC	YD
SPEED (KHZ)	1000	500	250	50
SET TIME (US)	1	2	4	17.5
LSO	52	10.4	20.8	92.5
ADS	75	1.5	3.0	13.4
0 UP	2	4	8	3.6
0 DOWN	2	4	8	3.6
RESET	1.5	3.0	6.0	26.8

SOURCE: PW IN 118

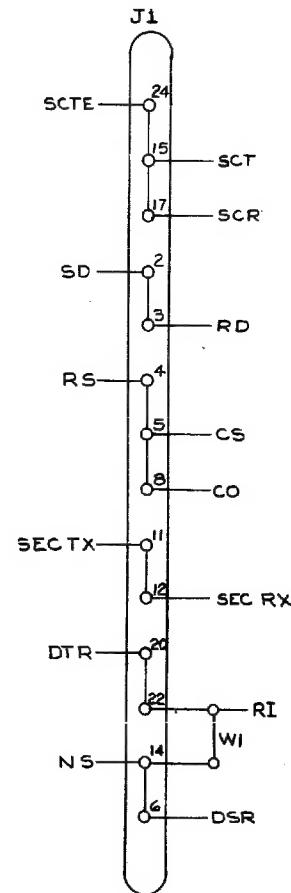
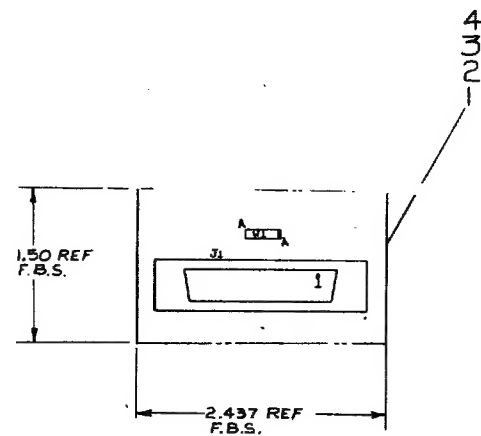
Clock SPECS:
±10% EXCEPT FOR
1MEG +10% ONLY

REVISIONS			TITLE		SIZE CODE	NUMBER	REV.
CHK	CHANGE NO.	REV.	D16-LU HI		DIG	D C S M8202-0-1	C
DEC FORM NO. 8			SCALE		SHEET 16 OF 16	DIST.	

DMC-47

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NOTES:



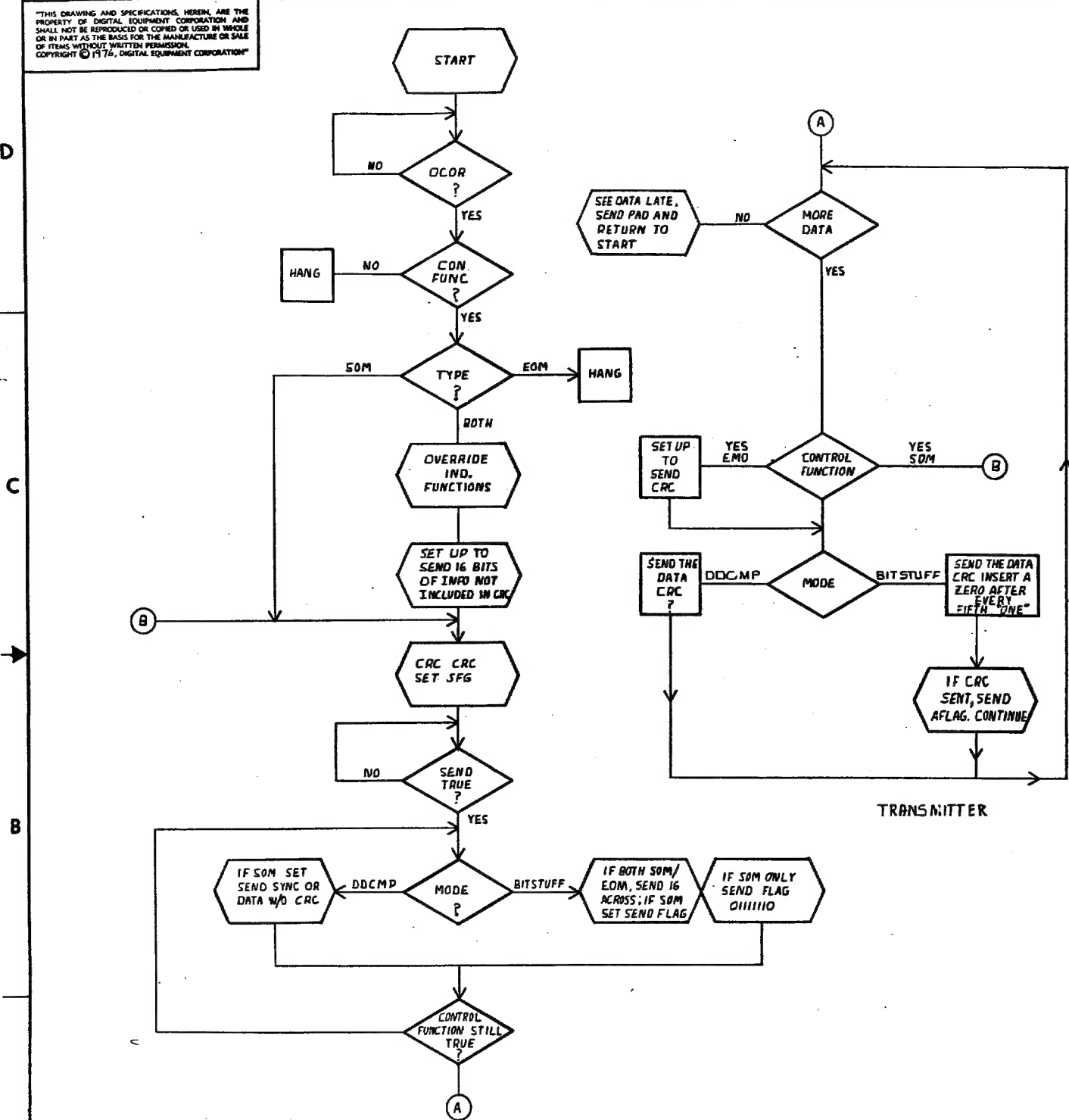
REF		X-Y COORDINATE HOLE LOC	KCO-H325-0-4	1
REF		ASSY/DRILLING HOLE LAYOUT	D-AH-H325-0-5	2
REF		MODULE ECO HISTORY	B-MH-H325-0-6	3
I		ETCHED CIRCUIT BOARD	5011698	4
I	JI	CONNECTOR	1210247	5
I	WI	INSULATED JUMPER WIRE	9009185	6

QTY	REF. DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
ETCH BOARD REV. C				
FIRST USED ON OPTION MODEL DVII				
REVISIONS				
DEC NO.	EIA NO.	DEC NO.	EIA NO.	REV.
SEMICONDUCTOR CONVERSION CHART				
NEXT HIGHER ASSY DUA-DVII-0-0				
SCALE 1 OF 1				
SHEET 1 OF 1				
DRN. DATE 1/23/78				
CHKD. DATE 1/23/78				
ENG. DATE 1/23/78				
PROJ. ENG. DATE 1/23/78				
PROD. DATE 1/23/78				
TITLE H325 TEST CONNECTOR				
SIZE CODE DCS				
NUMBER H325-0-1				
REV. B				

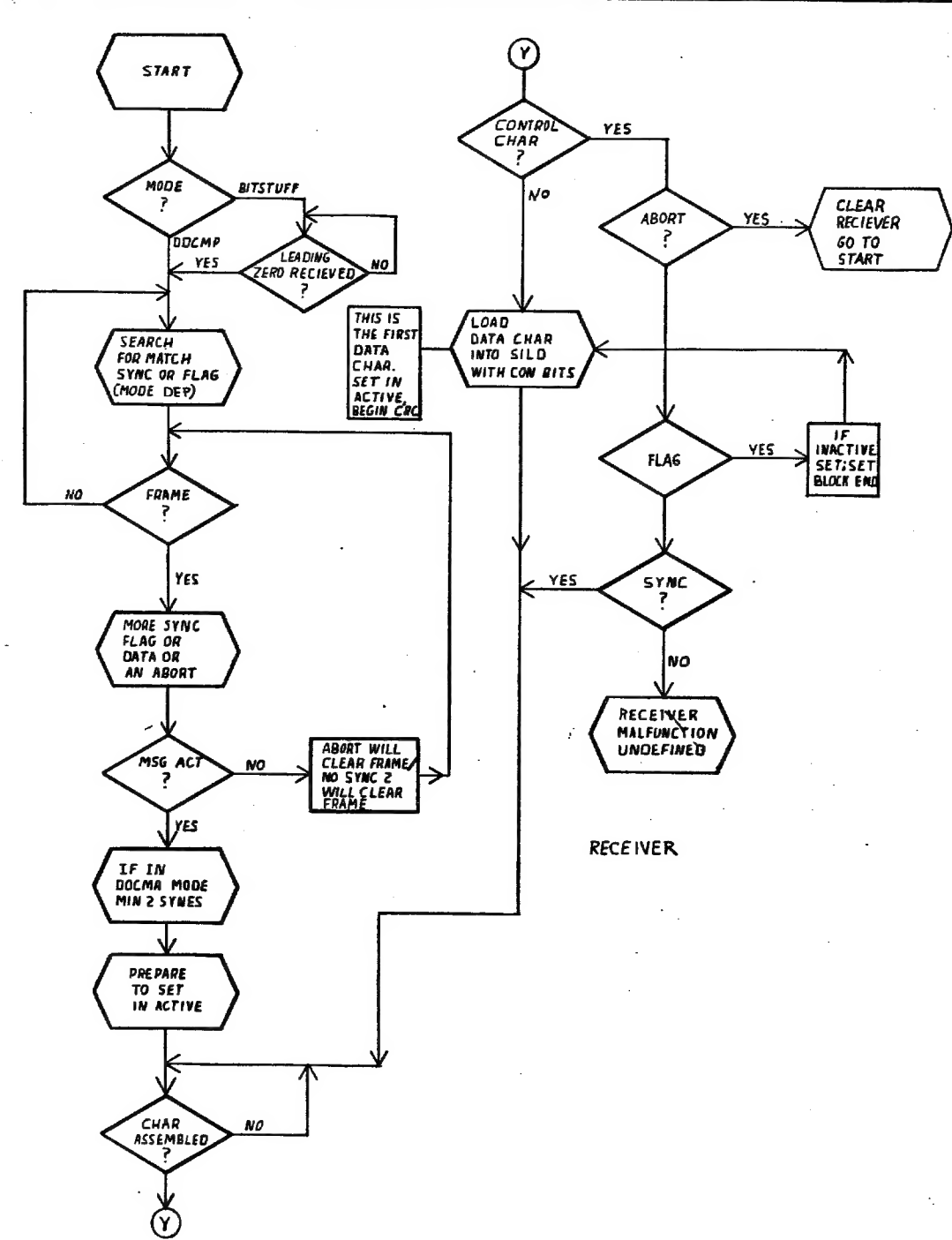
IC TYPE	GND	+5V
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY EXCEPTIONS ARE STATED ABOVE		
IC PIN LOCATIONS		

Dmc-49

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TRANSMITTER



RECEIVER

REV.	CHG.	DATE	BY
1		11-1-76	B. SMITH
2		3-22-76	B. SMITH
3		4-25-76	B. SMITH
4		4-25-76	B. SMITH
5		4-25-76	B. SMITH
6		4-25-76	B. SMITH
7		4-25-76	B. SMITH
8		4-25-76	B. SMITH

REVISIONS			
REV.	CHG.	DATE	BY
1		11-1-76	B. SMITH
2		3-22-76	B. SMITH
3		4-25-76	B. SMITH
4		4-25-76	B. SMITH
5		4-25-76	B. SMITH
6		4-25-76	B. SMITH
7		4-25-76	B. SMITH
8		4-25-76	B. SMITH

DRN. D. 12 JAN 76	FIRST USED ON	DMC 11
CHKD. J. 12 JAN 76	TITLE	TRANSMITTER FLOW
ENG. J. 12 JAN 76	PROJ. ENG. J. 12 JAN 76	PROD. R. 12 JAN 76
NEXT HIGHER ASSY.	SIZE	CODE
D-UA-M8202-0-0	D	CS
SCALE	NUMBER	REV.
SHEET 1 OF 16	DIST.	C

DMC-50